**OpenHW TV Series 1 E03**

**Episode 3 – CORE-V SW and HW in the FPGA environment**

We are ready to promote episode 03 of OpenHW TV which will air live on:

WHEN: **August 20 @ 11am EST/ 8am PST/ 4pm BST**

WHAT: **CORE-V SW and HW in the FPGA environment**

Please use the following resources to promote the event as widely as possible and attract registrations.

Be sure to tag OpenHW Group in your social media, use #OpenHWTV and #CORE\_V wherever possible and share posts that you see from OpenHW Group:

Twitter: @openhwgroup (<https://twitter.com/openhwgroup>)

LI: @openhw group (<https://www.linkedin.com/company/openhwgroup>)

**Registration link for OpenHW TV S1E02:**

<https://us02web.zoom.us/webinar/register/WN_DNpfU1JuRQWHAmzVlhqdjA>

OpenHWTV branding:





Social media posts:

*Twitter:*

* Don't forget to register for [#OpenHWTV](https://mattermost.openhwgroup.org/all-users/messages/%40rickoco) S1E03 on August 20 for complete info on the [#OpenHWGroup](https://mattermost.openhwgroup.org/all-users/messages/%40rickoco) Eclipse based IDE for [#CORE\_V](https://mattermost.openhwgroup.org/all-users/messages/%40rickoco) [#RISC\_V](https://mattermost.openhwgroup.org/all-users/messages/%40rickoco): <https://bit.ly/30KDkKW>
* For a detailed overview of the open source GNU toolchain for the [#CORE\_V](https://mattermost.openhwgroup.org/all-users/messages/%40rickoco) family of [#RISC\_V](https://mattermost.openhwgroup.org/all-users/messages/%40rickoco) cores provided by @embecosm, register now for [#OpenHWTV](https://mattermost.openhwgroup.org/all-users/messages/%40rickoco) S1E03 on August 20: <https://bit.ly/30KDkKW>
* [#OpenHWGroup](https://mattermost.openhwgroup.org/all-users/messages/%40rickoco) HW Task Group Chair from @CMCmicrosystems will present the Genesys2 demo platform at the next [#OpenHWTV](https://mattermost.openhwgroup.org/all-users/messages/%40rickoco) live broadcast on Aug 20 and answer your questions. Register now: <https://bit.ly/30KDkKW>
* Do you want to contribute to a thriving ecosystem for [#CORE\_V](https://mattermost.openhwgroup.org/all-users/messages/%40rickoco) [#RISC\_V](https://mattermost.openhwgroup.org/all-users/messages/%40rickoco) software tools, models and operating systems? Find out how to get involved at the next [#OpenHWTV](https://mattermost.openhwgroup.org/all-users/messages/%40rickoco) live broadcast on Aug 20: <https://bit.ly/30KDkKW>
* [#OpenHWTV](https://mattermost.openhwgroup.org/all-users/messages/%40rickoco) S1E03 will feature a live demonstration of the [#Riscfree](https://mattermost.openhwgroup.org/all-users/messages/%40rickoco) Eclipse based IDE debug interface from @ashlingsystems to the Genesys2 [#FPGA](https://mattermost.openhwgroup.org/all-users/messages/%40rickoco) board, with the chance to ask questions to the live panel. Register now at: <https://bit.ly/30KDkKW>

LinkedIn

* OpenHW TV S1E03 will feature a live demonstration of the Riscfree Eclipse based IDE debug interface from @ashlingsystems to the Genesys2 FPGA board, with the chance to ask questions to the live panel. Register now as spaces are limited at: <https://bit.ly/30KDkKW>
* For a detailed overview of the open source GNU toolchain for the CORE-V family of RISC-V cores provided by OpenHW Group member @embecosm and an in-depth look at the Genesys2 demo platform, register now for OpenHW TV S1E03 on August 20 - 11am EST/4pm BST/8am PST: <https://bit.ly/30KDkKW>