2023-August-24

Agenda Topics	Moderator	Minutes
Approval of minutes from August 17th, 2023 meeting	George	5
Website Redesign update	George/Hendrik	10
Informational: Azure Credit renewal	George	5
Use QEMU for Jenkins runner	Ludovic	5
AOB		

Quorum (Quorum is 50% of Committee)**:**

(5/10) required for simple majority votes.

(7/10) required for super majority votes.

Attendees

Steering Committee Members

Company	Primary	Alternate
Microsoft	☑ George Adams	☐ Martijn Verburg
Red Hat	☑ Tim Ellison	☑ Shelley Lambert
IBM	☑ Murali Veeravalli	☐ Lan Xia
Committer Rep	☑ Stewart Addison	
Alibaba Cloud	☑ Sanhong Li	☐ Denghui Dong
Huawei	☐ Chen Rui	
Azul	☑ Simon Ritter	☐ Gil Tene ☑ Gerrit Grunwald
Rivos	☑ Ludovic Henry	☑ Tony Printezis
Google	☐ John Pampuch	☐ Dan Gazineu ☑ Dave Hensley
Open Elements	☑ Hendrik Ebbers	

Other Attendees

Company	Name	Name
iJUG	☑ Jan Westerkamp	
Bloomberg	☐ Hector Geraldino	
Canonical	☑ Samir Kamerkar	☑ Pushkar Kulkarni
Eclipse Fdn	☐ Carmen Delgado	☐ Paul Buck
Eclipse Fdn	☑ Thabang Mashologu	☐ Sharon Corbett
Eclipse Fdn	☐ Mike Milinkovich	☐ Paul White
Eclipse Fdn	☑ Juan Rico	

Minutes:

Approval of minutes from August 17th, 2023 meeting
- Approved

Website Redesign update
- George shared a document showing three different quotes. ([Link](#))
- The marketing subcommittee voted in a previous meeting to select Offer 2 which is a design agency for €6.2k. This doesn't include illustrations or implementation.
- A discussion was held around the benefits of going with the specific design agency rather than a freelancer.

Informational: Azure Credit renewal
- Microsoft has renewed the Azure credits sponsorship for another 12 months at $85k.

AOB
- The WG welcomed Gerrit Grunwald as an alternative secondary representative for Azul
- A [regression](#) in OpenJDK was announced recently. We will need to respin Temurin 11 and 17.
- The project received a vulnerability report and is handling as per process.

Use QEMU for Jenkins runner
- Rivos has been struggling to get RISC-V machines for our CI system.
- They would like to explore emulating RISC-V on an x86 machine. This will be slightly slower but with a high core count it should be suitable.
- It was suggested that this topic be brought to the PMC for further technical discussion.