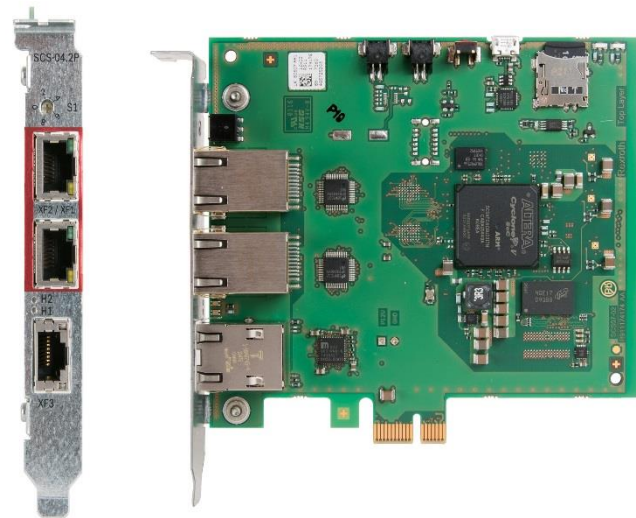


Sercans product family Easy. Complete. Independent. *Lean*.



Sercans XS* (Soft & Lean) and Sercans S/M/L (Fast & Easy)

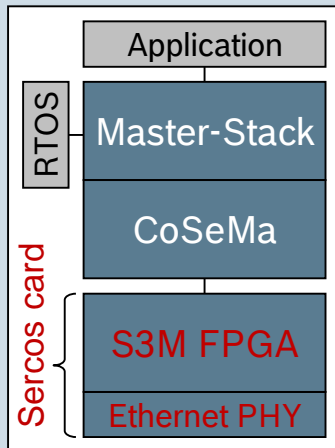
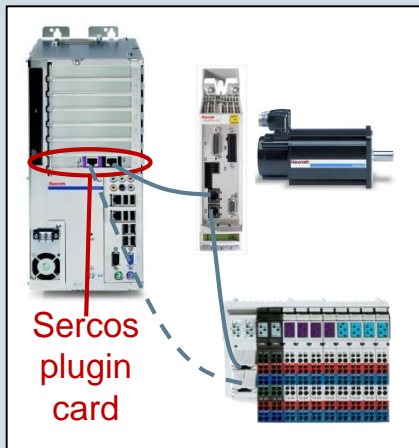
Sercans product family | Status: 2016-07-28

* In definition phase, partially available as evaluation version under customer agreement

** Drive&Control business of Bosch Group – Bosch Rexroth = The Drive&Control Company

Advantages Sercos SoftMaster

Hard Master Sercans S, M, L



- + Higher synchronicity
- + All Sercos features possible
- + Lower SW real-time requirements

Advantage:

Competitive master price
Reduction of size*
Keep main Sercos Pros

Result:

- 1) New application fields
- 2) New customer groups
- 3) More Customer satisfaction

Std. Ethernet

- + Lower HW cost
- + Reduction of mechanical controller size (no Sercos plugin card)

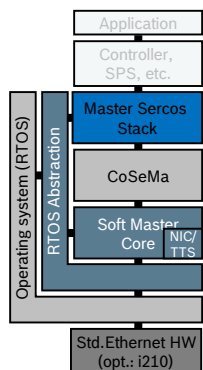
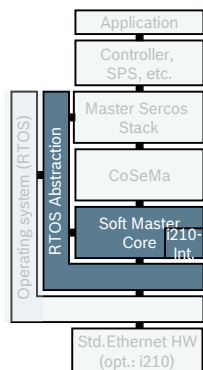
RTOS: Real-Time Operating System

NIC: Network interface card

TTS: Time Triggered Send

* Prototype

Sercans XS vs. SoftMaster Core



Sercos Soft Master Core (Serial status)

- Soft-Master FPGA emulation only
- Support via SourceForge mailing list
- **Customer value:**
 - Best price solution possible!

Advantage: **Differentiation of the offer**
 Result: **Let the customer decide**
There are arguments for each solution

Sercans XS (Well approved prototype)

- Incl. Soft-Master-Core, RTOS-Abstr., Sercos III Stack
- **Economy:** Easy-to-use stack, sample program, Standard Ethernet, down to 500µs cycle time (prototype, well approved)
- **Basic:** Added: NIC triggered timing (TTS), down to 125µs cycle time, Ring redundancy, UCC via master (prototype, in approval)
- **Advanced:** Compatibility to Sercans HW solutions, minimum application programming efforts (**still in definition phase!**)
- Evaluated hardware package available (var. HMI controls)
- Support (multi-level tech support)
- **Customer value:**
 - Shortest time to market!
 - Best Performance!



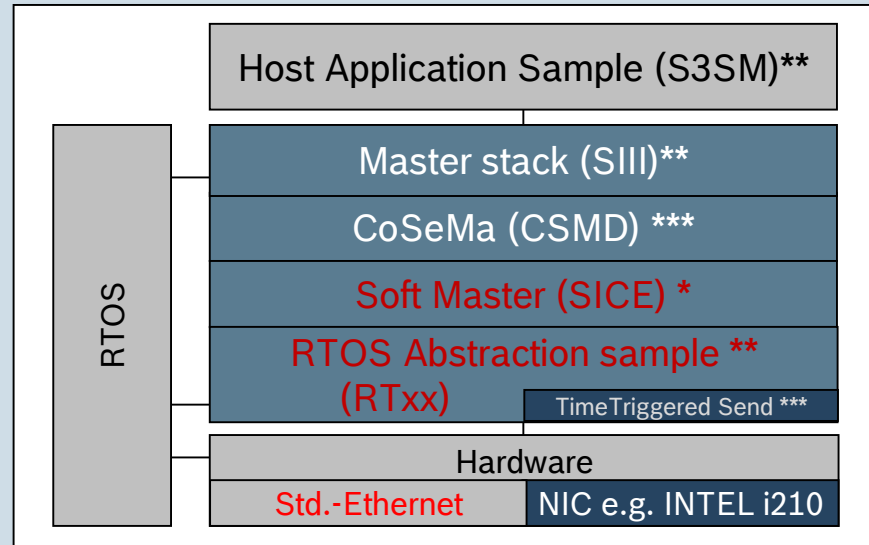
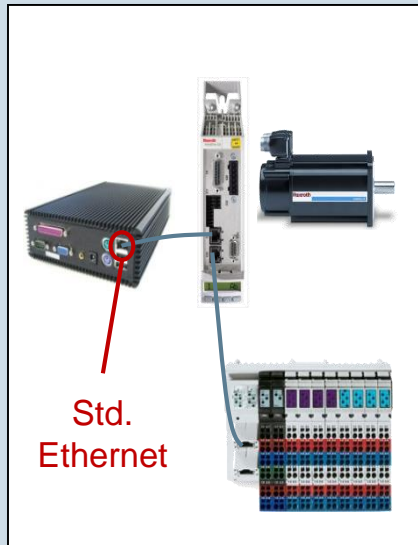
RTOS: Real-time Operating System; **UCC:** Unified Communication Channel (Standard Ethernet transmission)

CoSeMa: Common Sercos Master is a free available middle-ware library for Sercos on sourceforge

Electric Drives and Controls | | F. Scheurer | 2016-07-28 | DC-IA/SPC | For addressee only | © Bosch Rexroth AG 2016. All rights reserved, also regarding any disposal, exploitation, reproduction, editing, distribution, as well as in the event of applications for industrial property rights.

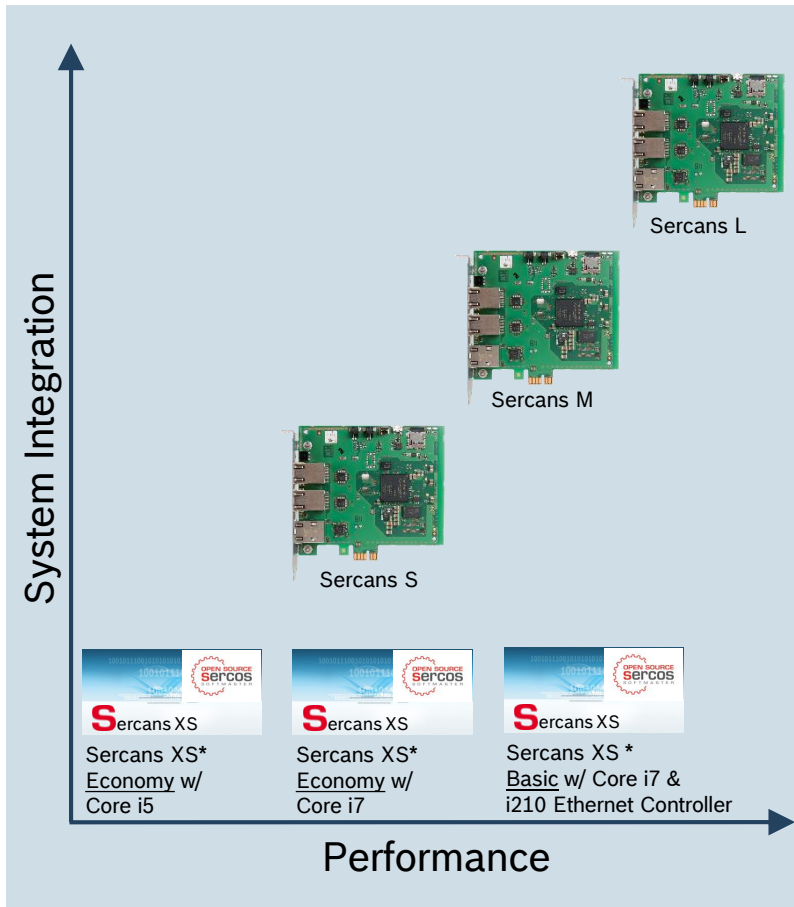
Sercos Soft-Master as Sercans XS*

Sercos SoftMaster comfort package (Status 2016-07)



RTOS: Real-Time Operating System NIC: Network interface card timing *** AV Series status
* PT Prototype ** EW Prototype *** **1st real Application of AVB/TSN Technology**

Scalable performance for all applications



Sercans XS* – Standard Ethernet Controller

- Cost-efficient solution
- Connectable devices system dependant

Sercans S – PCIe card

- Ideal for small, simple series machines
- Up to 16 devices connectable

Sercans M – PCI / PCIe-card


- Standard type for most applications
- Up to 99 devices connectable

Sercans L – PCI Express-card

- Ideal for big, complex, high-end-systems
- Extended data throughput to application (factor 10 compared to Sercans M)
- Easy integration in operating system / application
- Up to 511 devices connectable

* Sercans XS still in **prototype** phase; well approved, re-prioritization upon request

Sercos software contributions



S **Sercos UCC Ethernet Network Driver**
 Example for a sercos III Network Driver
 ...This project contains an example Code of an Ethernet Network Driver for the **Sercos** III network which ...
 11 weekly downloads

S **Common Sercos Master API**
 Software library for Sercos Industrial Ethernet master implementations
 ...The Common **Sercos** III Master is a middle-w are library for the real-time field-bus **Sercos** III. Together ...
 9 weekly downloads

S **Sercos SoftMaster Core**
 Software-based Sercos Industrial Ethernet Master
 ...The **Sercos** SoftMaster core is intended for implementing a software-based **Sercos** Industrial Ethernet ...
 2 weekly downloads

S **Sercos Internet Protocol Services**
 ...The **Sercos** Internet Protocol Services (IPS) specifies different services for UC Channel communication ...
 5 weekly downloads

* Sercos Config Tool is still in project preparation phase!

- **Bosch Rexroth decided to leave Sercos technology distribution to technology providers (with Sercans as exception):**
 - Cannon Automata
 - Hilscher
 - HMS
 - Other Processor/logic manufacturers
- **Established ways to distribute Bosch Rexroth's contribution**
 - Contribution to **consortium development** (CIP-Safety, Sercos Monitor, Sercos Config Tool*) w or w/o **license fees in favor of Sercos International**
 - Provision of Master/Slave **IP Core** to Sercos Technology provider(s) with **license fees in favor of Sercos International**
 - **Open source projects at SourceForge** (CoSeMA, S/IP Client, SoftMaster Core, UCC Virtual Ethernet Driver)

Sercos SoftMaster Open Source Project (1)

Press Release

Sercos III Soft-Master-Core Open Source announced

Sercos International (SI) has announced that in cooperation with Bosch Rexroth a Sercos III Soft-Master core will be provided as an open source software.

By using a Sercos III Soft-Master, a Sercos III master device can be implemented without a specific Sercos III hardware controller in the form of FPGAs or ASICs. Instead, a standard Ethernet controller is used and the Sercos III hardware functions are emulated in a host-based driver software. With this implementation approach a sufficient real-time performance can be ensured for a large number of applications. If an Ethernet controller is used that operates with multiple queues and a telegram scheduler (as e.g. the INTEL i210™), a synchronicity similar to that of a hardware-based master can be achieved.

Sercos
the automation bus

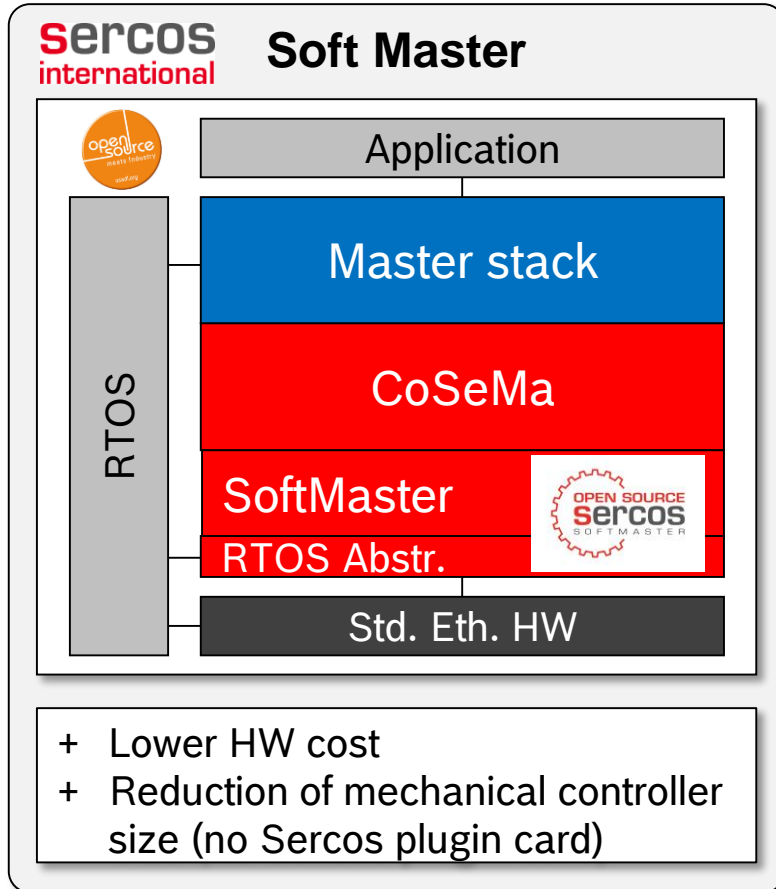
April 13, 2015

Page 1 of 2

Sercos
international



Sercos SoftMaster Core Open Source Project (2)



- Open Source Project started in cooperation with Bosch Rexroth and OSADL (planned cooperation with Schneider Electric)
- SoftMaster Core supporting Standard Ethernet controller & Intel i210
- OS independent due to RTOS abstraction
- Compatible to CoSeMa 5V3 and 6V1
- With Std. network controller:
 - Packet jitter achievable $\sim 20..60\mu s^*$
 - Slave-Synchronicity achievable $\sim 1\mu s^*$
- With NIC Timing and TTS (e.g. INTEL i210): almost HardMaster performance ($\ll 1\mu s$)

*) depending on platform (hardware & OS)

Sercos SoftMaster Core – Basis of Sercans XS



The image shows a screenshot of a SourceForge search results page. At the top left, there is a logo for 'OPEN SOURCE Sercos SOFTMASTER' featuring a red gear icon. To the right of the logo is a photograph of a Rexroth Sercos III network driver hardware unit. Below the logo and photo, there is a list of search results for Sercos-related projects. A large, stylized grey arrow points from the 'Sercos SoftMaster Core' result to the right, towards the main text of the slide.

Project Name	Description	Downloads
Sercos UCC Ethernet Network Driver	Example for a sercos III Network Driver ...This project contains an example Code of an Ethernet Network Driver for the Sercos III network which...	11 weekly downloads
Common Sercos Master API	Software library for Sercos Industrial Ethernet master implementations ...The Common Sercos III Master is a middle-w are library for the real-time field-bus Sercos III. Together ...	9 weekly downloads
Sercos SoftMaster Core	Software-based Sercos Industrial Ethernet Master ...The Sercos SoftMaster core is intended for implementing a software-based Sercos Industrial Ethernet...	2 weekly downloads
Sercos Internet Protocol Services	...The Sercos Internet Protocol Services (IPS) specifies different services for UC Channel communication...	5 weekly downloads

- **Sercos SoftMaster** provides an interface to the **Common Sercos Master API (CoSeMa)** which is available as **open source** software on **Sourceforge**: [cosema.sourceforge.net](https://sourceforge.net/projects/cosema)
- **CoSeMa**
 - This master function library contains API routines for **initializing, phase sequencing, timing calculation** and functions for **cyclical and acyclic communication**
 - **Programming language: C**
 - Independent of operating system
- **Sercos SoftMaster Core**
 - Added 12/2015 as extra project to SourceForge

Open source licensing – Basis of Sercans XS



License handling of the Open Source Sercos Softmaster

- Subject to open source license: Sercos Soft Master Core
- Technically: Sercos IP Core Emulation (S.IC.E)
- License model: Changed from LGPL to MIT
- Effective: 2015-12-15
- Programming language: C
- Independent of operating system
- Further software components are prepared to follow

Broad RTOS support – Basis of Sercans XS*

- Extension to IPC IndraControl VPB/VPP
- Minimum cycle time: 500µs ... 1 ms
- Support of topology depends on realtime capabilities of the Ethernet controller (Standard or INTEL i210)
- Tested on IndraControl VPx with RTOS:
 - INtime for Windows from tenAsys
 - Windows 7 ext. RTX from IntervalZero
 - Linux with PREEMPT_RT patch
 - QNX Neutrino
 - Windows Embedded Compact 7
 - VxWorks
- Improved Performance by NIC-based precision timing, supporting TTS (e.g I210 from Intel or similar)

* Well approved Prototype!

RTOS: Real-Time Operating System

NIC: Network interface card/controller

TTS: Time Triggered Send

Broad HW approval – Basis for Sercans XS*

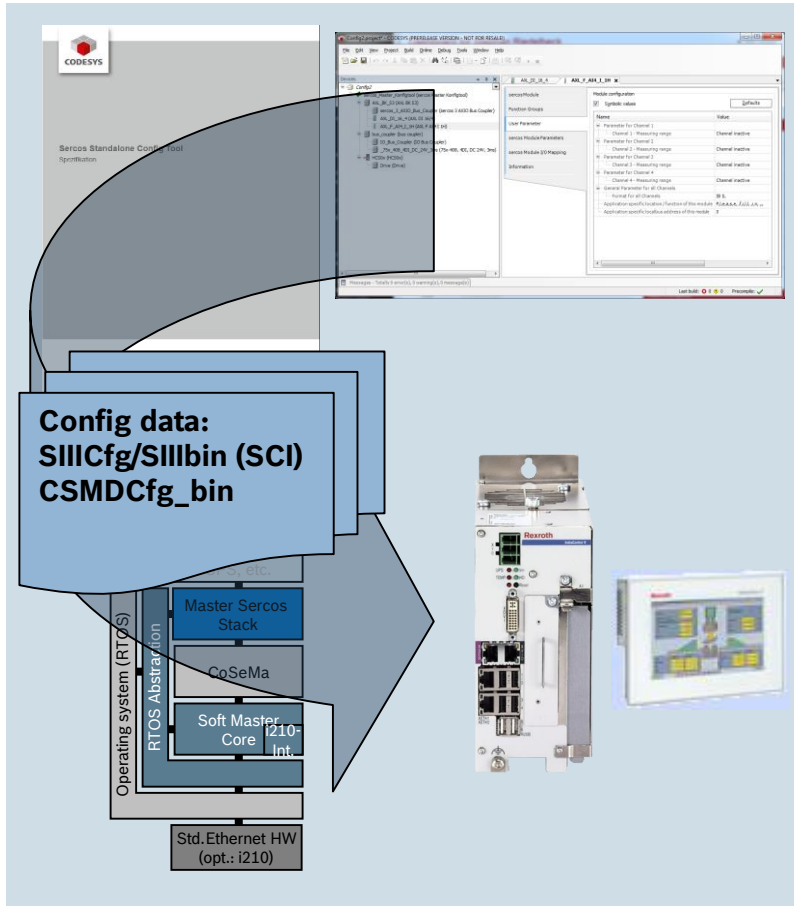


- **Industrial PC x86**
 - 32/64 Bit
 - IndraControl VBP / VPP
 - IndraControl VEP
 - Siemens IPC
 - Desktop Office PC
- **ARM**
 - A8 and A9
 - Beaglebone Black **
 - IndraControl VR21 **

* Well approved Prototype!

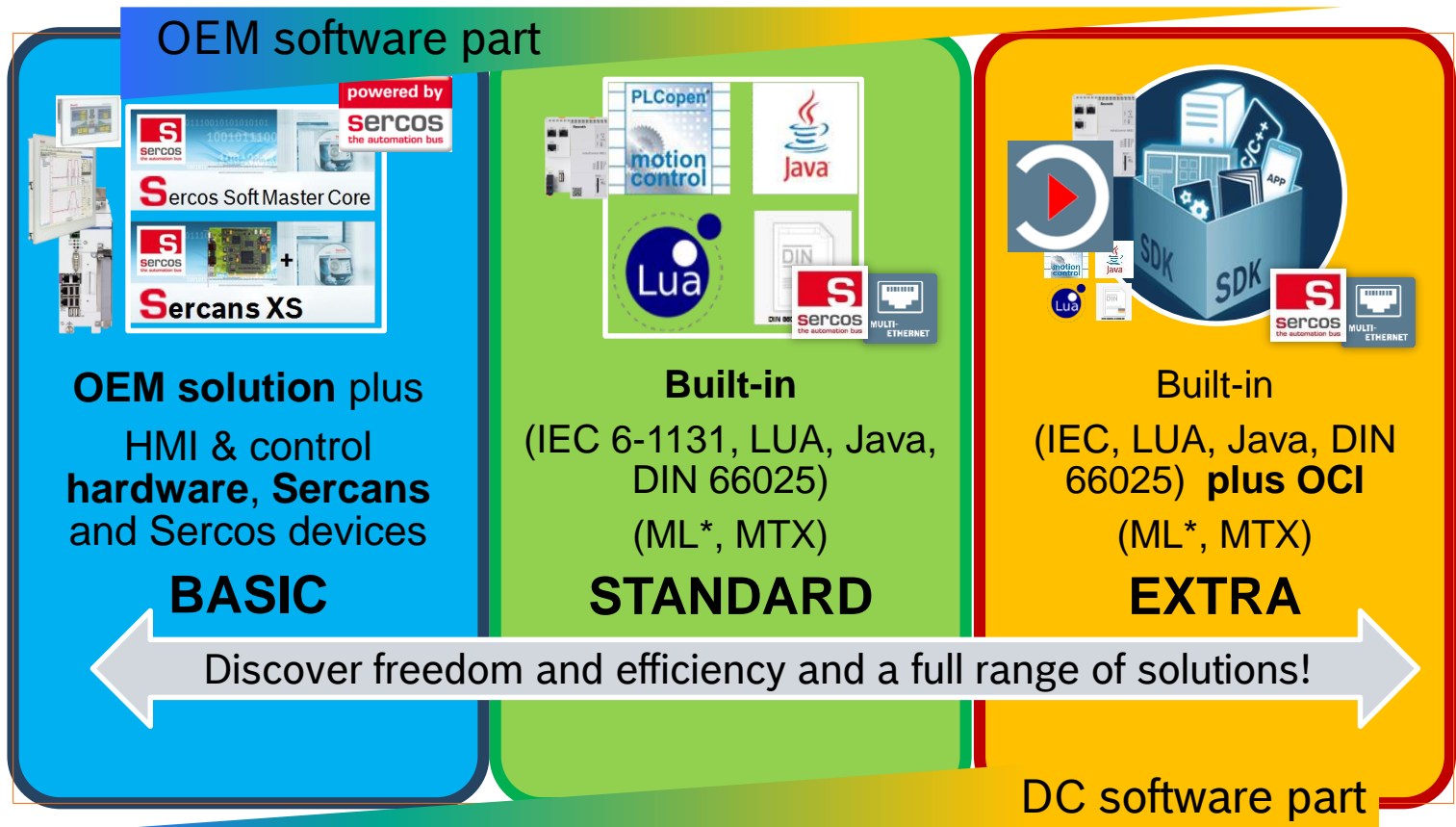
** Under evaluation

Sercos Config Tool (in preparation)



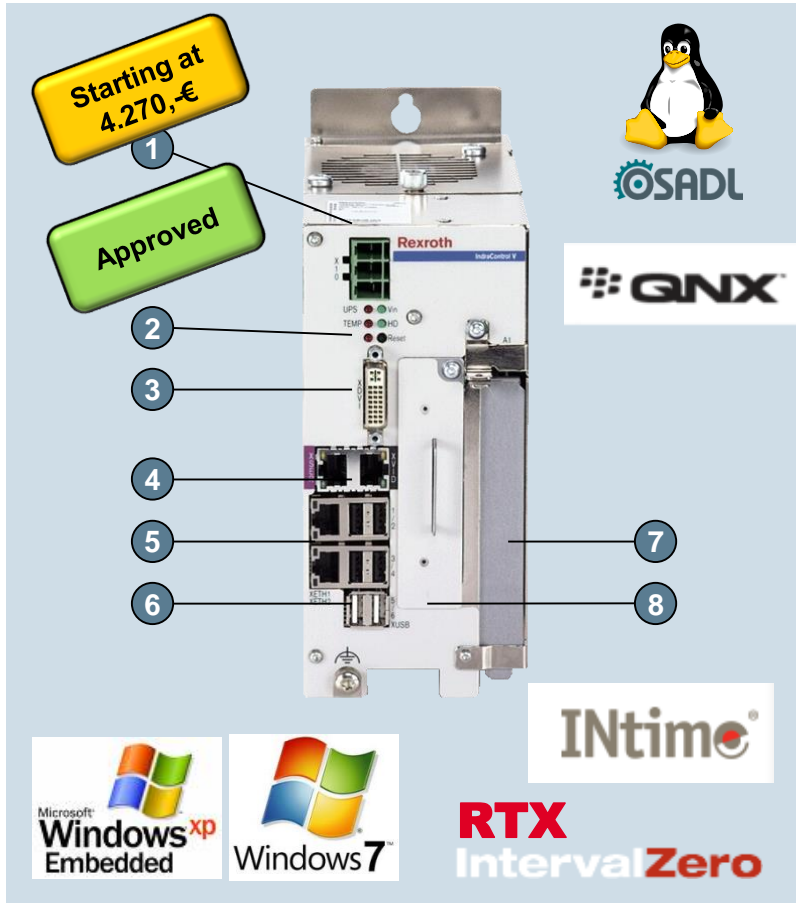
- **Flexible Config of OEM controls**
- **Consortium (Sercos International, 3S)**
 - Rovema
 - Phoenix Contact
 - Bosch Rexroth
- **Implementation**
 - 3S
 - Shared costs (estimated 60 k€)
 - Consortium members appreciated
- **Functions**
 - Offline configuration (CoDeSys S3)
 - Online (Scan, Parameter r/w, Diagnostics)
 - IO-Link gateway

Sercans inside DC's open solution portfolio



Bosch Connect: Sercos SoftMaster opens door to challenging high volume applications

Advanced solution VPB 40.3 *



- Advantage:
All-inside (HMI, Sercans XS and Sercans S/M/L) Competitive Price
Result:
Customer can concentrate on his solution!
1. 24 V DC power supply
 2. Status-LED
 3. DVI
 4. CDI-Interface for operator display
 5. 2 x GBit LAN
 6. 6 x USB 2.0
 7. Slot
 - 1xPCI
 8. Mass storage
 - Hard disk
 - Hard disk RAID 1 (Option)
 - Solid-State-Disk (Option)

Follow up: VPB 40.4 / PRxx (definition phase)

* Prototype

Advanced solution VR21 **

Advantage:
 Resistive single touch functionality
 4,3" widescreen display
 resolution: 430x272
 colors: 16 bit
 Demo option & Competitive Price
 7"- widescreen display
 resolution: 800x480
 colors: 16 bit
Result:
 9"- widescreen display
 resolution: 800x480
 colors: 24 bit

Customer can concentrate on his solution!

Starting at 1.280,-€

In prototype approval! High volume only!

INTEGRATED PLATFORM ADDRESSES HARD REAL-TIME SYSTEM REQUIREMENTS

Customer Application Platform		
Non-Real-time Windows Applications	Non-Real-time Windows Applications (RTOS or RTAA)	Real-time Applications (Microkernel and SMP aware)
Hard Real-time Platform		
Windows World Class User Interface	I/O	Network
Windows API	Stack	Drivers
Microsoft Windows Kernel	3rd Party Apps	
Core 1	IntervalZero RTx or RTAA API	
Core 2	IntervalZero RTx or RTAA Real-time Kernel Extension	
Core 3	Core 4	Core 5
Multi-core Multiprocessor Hardware Platform		
ARM (Future)	x86	x64

OSADL

Windows Embedded Compact 7

IntervalZero

Technical data

- Housing: galvanised steel
- CPU **ARM Cortex-A8** 800 MHz.
- 512 MB RAM, 256 MB Flash
- 1x Ethernet 10/100 Mbit
- 2x USB 2.0 Host
- power supply 24V DC

** Example, HW-Base in definition phase, not scheduled

Overview and comparison

	Sercans XS <u>Economy</u> (SoftMaster)	Sercans XS <u>Basic</u> (SoftMaster)	Sercos-FPGA / Sercans S, M, L (HardMaster)
Architecture			
HW	1 Std.-Ethernet-Controller	2 * INTEL i210-Ethernet-Controller (or TTS* capable HW)	Sercos-FPGA; 2 * Ethernet-PHY
Pros	<ul style="list-style-type: none"> • Std.-HW, div. types • Typ. Cost benefit 	<ul style="list-style-type: none"> • Quasi-Std.-HW • Typ. Cost benefit • High synchronicity (Jitter <150ns or even 40ns) • UCC@Master supported • Ring redundancy 	- Benchmark reference -
Cons	<ul style="list-style-type: none"> • Lower synchronicity • Curr. only line topology • UCC@Master not solved <u>yet</u> • Min. Cycle ~ 500µs • CPU-load SoftMaster <5% • CoSeMa 5V3 and 6V1 	<ul style="list-style-type: none"> • Min. cycle ~125µs • CPU load SoftMaster <5% • CoSeMa 5V3 and 6V1 	- Benchmark reference -

NIC: Network interface card/controller (precise Timing)

TTS: Time Triggered Send

Technical Comparison

	Sercans XS <u>Economy</u> (SoftMaster)	Sercans XS <u>Basic</u> (SoftMaster)	Sercos-FPGA / Sercans S, M, L (HardMaster)
Architecture			
HW	1 Std.-Ethernet-Controller 	2 * INTEL i210-Ethernet-Controller (or TTS* capable HW) 	Sercos-FPGA / Sercans PC-board; 2 * Ethernet-PHY
Cycle time	500µs	125µs	31,25µs FPGA / 62,5µs; 125µs; 2ms (Sercans L, M, S)
No. devices	Only limited by Host	Only limited by Host	- / 255, 128, 16 (Sercans L, M, S)
Synchronicity	20...50µs (~1µs by device PLL)	20...40ns	20...40ns
Topology	Line	Ring (Redundancy), Line, Dual Line	Ring (Redundancy), Line, Dual Line
UCC	Within Line	Routing@Master, Within Ring/Line	Routing@Master, Within Ring/Line
CPU Load	<5% Load on Host	<5% Load on Host	Minimum load on Host

NIC: Network interface card/controller (precise Timing)

TTS: Time Triggered Send

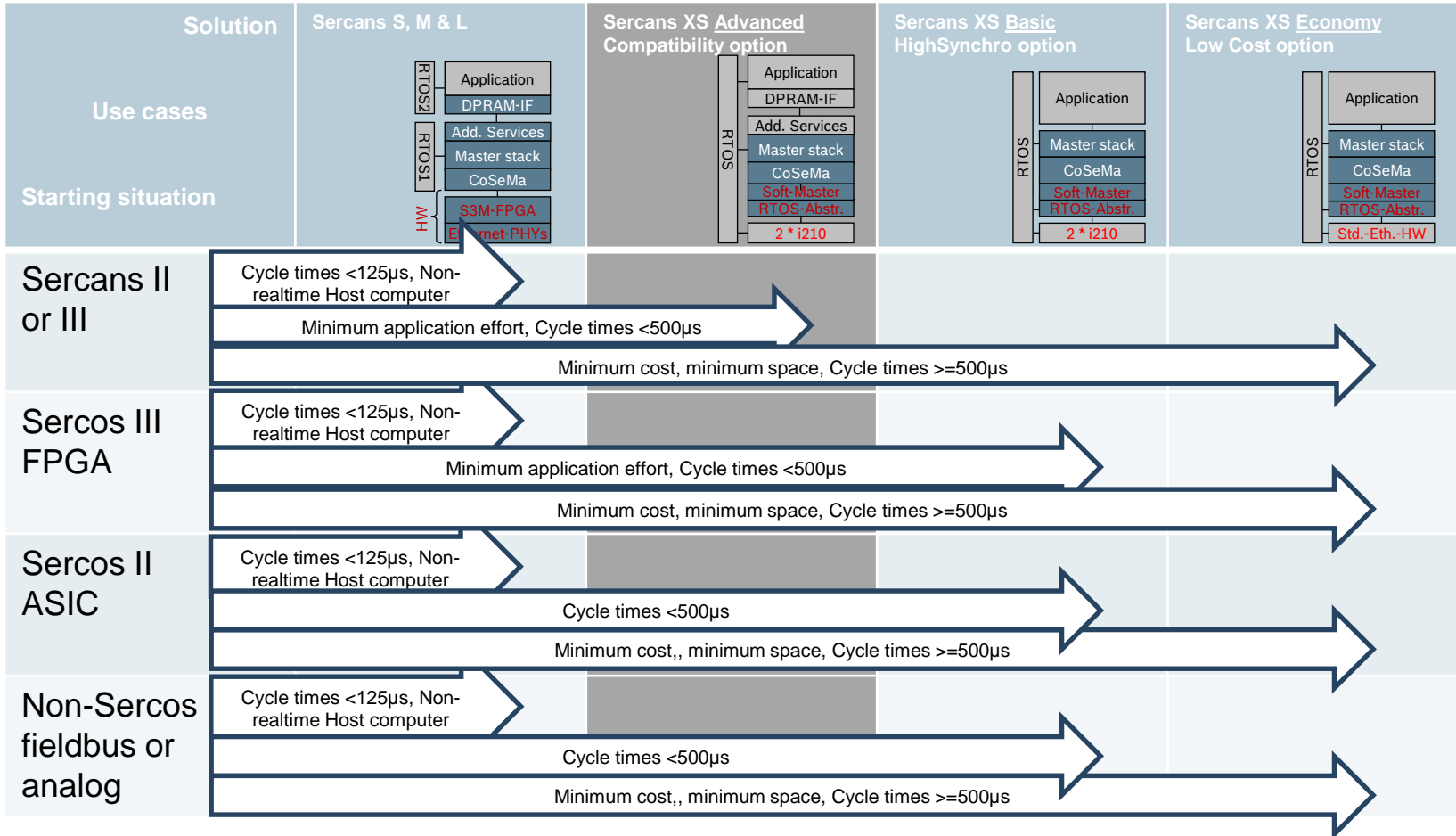
Benefits for Multiplier & OEM

* Definition phase

** Evaluation in progress

- **Customer value**
 - Open Source, freely available Sercos SoftMaster
 - Fitting into suite of other Open Source Software (CoSeMa, S/IP Services, UCC-ETH Driver)
 - RTOS and platform independent C Code
 - 6 supported RTOS
 - 2 supported HW platforms
 - Full line solution concept Sercans XS to L *
 - Hardware package option VPx/VR(high volume) *
 - Maximum component support (Drive, IO, Process)
- **Performance**
 - HardMaster synchronicity (NIC based TTS, e.g. i210)
 - Extendibility to Sercans S/M/L *
- **Ease of use, flexibility**
 - UCC always available (Engineering with full Ethernet capabilities)
 - Software compatible Sercans XS to L *
 - Software tested on Hardware package option **

Use cases of migration – where to go?



Currently Available AV, PT or EW

Currently not available, up to application

N/A as DC product

Sercos SoftMaster on Tour (Hanover fair 2016)



Hannes Richter (ISG, 2nd right) explains INtime-based SoftMaster demo of ISG's NC-Kernel, Virtuos, Phoenix Contacts eCLR and Bosch Rexroths IndraDrive and SafeLogic Compact to **Gary Liang (Phoenix Contact China, 2nd left)** accompanied by **Kim Hartman (left)** and **Andreas Knappe (TenAsys, right)**

Sercans XS in-a-box goes on tour to Asia



Components:

- VPB40.3 i7-8core, PCIe
- INTEL i210 T1 network card
- OCE demo suit case
- VDP15

Tour Plan:

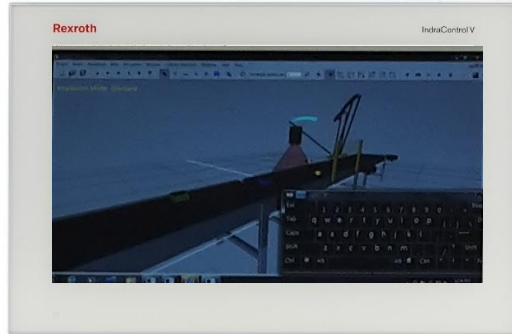
- ION (Japan)
 - 07/27/2016 Tokyo
 - 08/24/2016 Nagoya
- Automation (India)
 - 08/22-25/2016 Mumbai
- IAS (China)
 - 11/01-05/2016 Shanghai

Demo Software:

- ISG NC-Kernel
- Phoenix Contact Software
ProConOS eCLR PLC runtime
- ISG Virtuos simulation & commissioning
- TenAsys INtime RTOS



Open Core Engineering with Open Standards



Multi-Touch Display
VDP15.3

Supported by:



tenAsys
Embedded Virtualization Solutions



Industrial PC
VPB40.3
I7 8core



Servo Drive
IndraDrive Cs



Servo Motor
IndraDyn S
MSK030



Linear module
MKK40

Space* for your application

Select from thousands of components**

* Sercans XS needs << 5% of resources on an IPC

** from the whole Sercos Eco-system including Bosch Rexroth

Sercos goes Soft – a lean & scalable IE solution *



... makes your solution as light that you can fly!



Graphics Source: <http://openclipart.org/detail/28921/plane>

- Unlimited Commercial Use
- All Clipart are Released into the Public Domain

Sercans Master distribution concept

Option: Reduce risk by approved Rexroth IndraControl V platform

More Sercos platforms.
More functions, freedom, service and added value!

Sercos 100% performance, less costs

MLC/MTX S3 Drive	CoDeSys IO-Driver	ProConOs Communication Stack	TwinCAT Automation device driver	S5/S7 Device driver
IndraWorks	Sercans & Sercos SoftMaster Library	Sercos Config Tool		

Definition / Prototyping

Sercans XS (soft) **Rexroth: System 2017**

Sercans S (passive/active, standard) **Rexroth: passive used**

Sercans M (active, advanced)

Sercans L (active, high performing)

All system components available

* <http://de.codesys.com/produkte/codesys-fieldbus/echtzeit-ethernet/sercos.html>

** interest expressed by 3rd party

A free software model for Sercans XS

Stimulate by

- Low evaluation investment
- Obstacle-free start

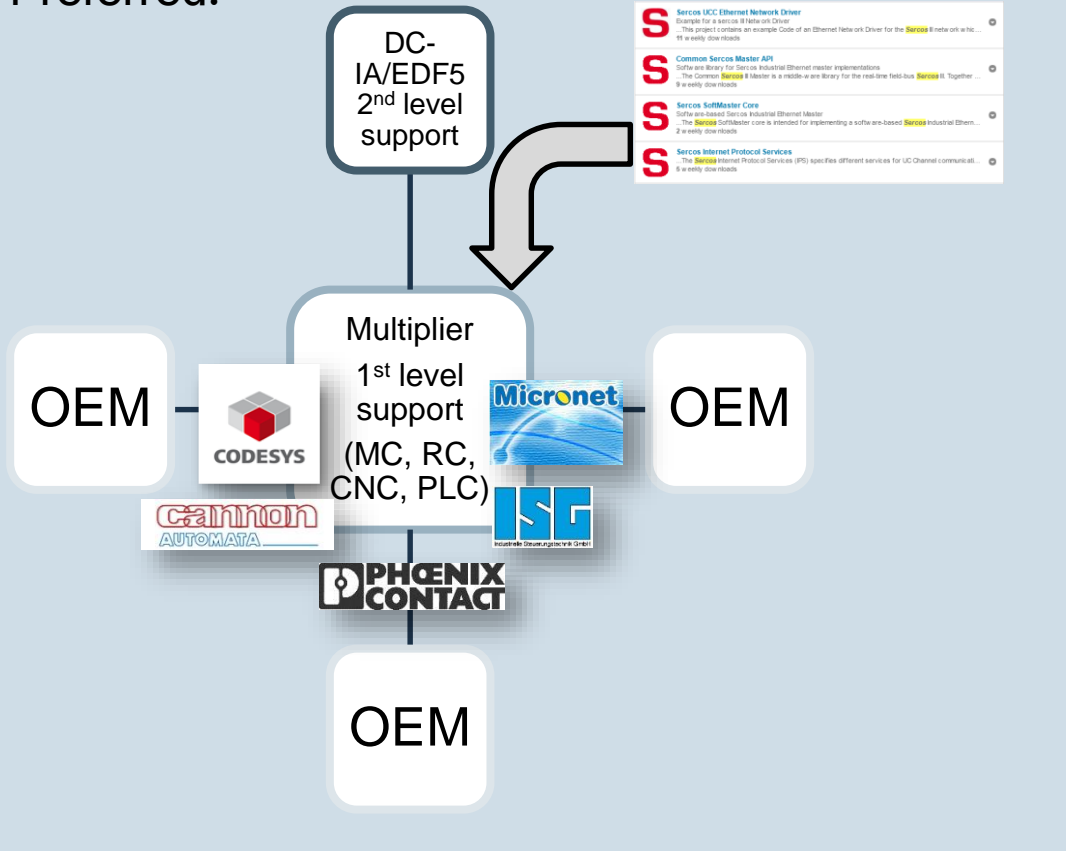
Honest deal of

- Training, Support & Service charge
- Mutual benefit out of continuous improvement

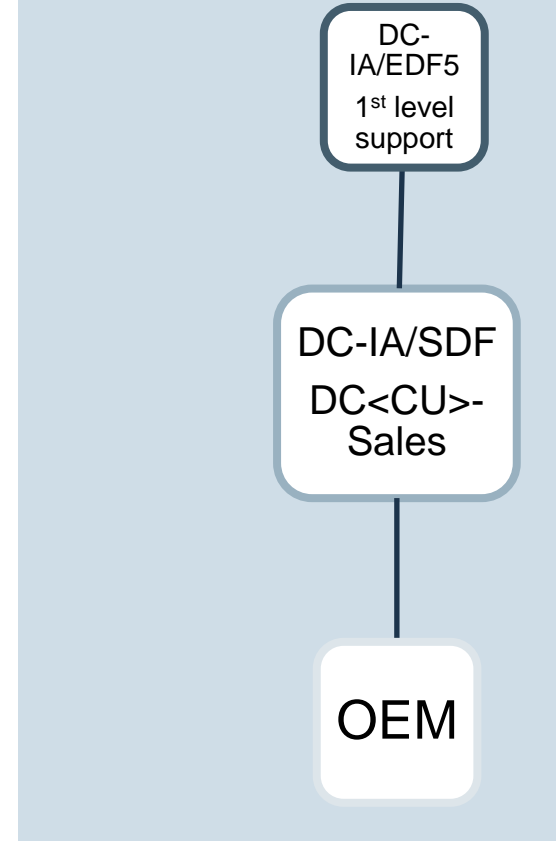


Multiplier and (OEM) Direct sales model

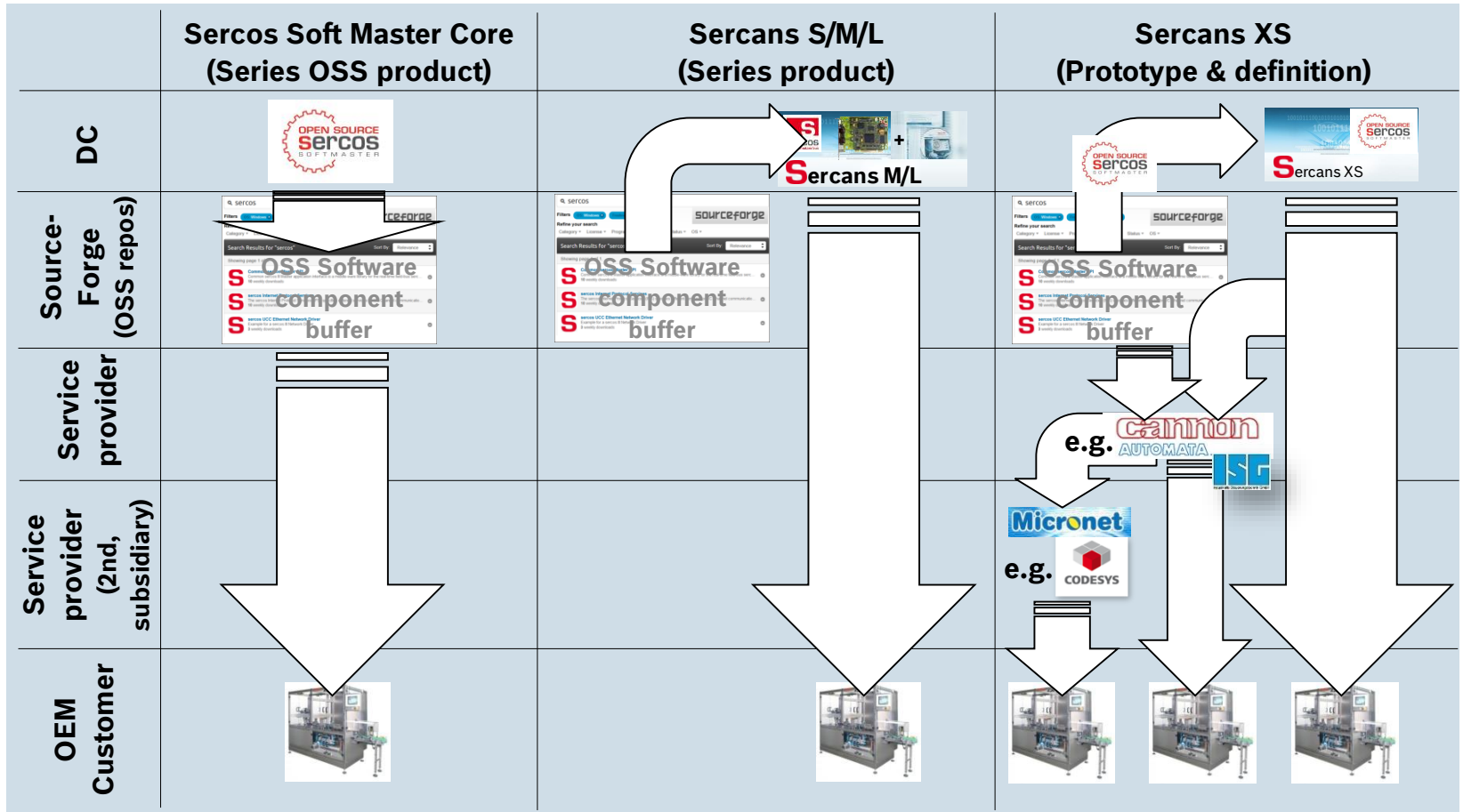
Preferred:



On Demand:



Sales and support chains



Training, support and service projects

Order position	Content	Quotation
Initial workshop (Kick-off on-site)	<ul style="list-style-type: none"> • Task analysis • Conceptual design • Additional code module definition • Effort estimation • Schedule definition <p><u>Result:</u> Quotation for final project</p>	Please contact your local Bosch Rexroth distributor or country unit! *
Integration support (Phone, Webex, Skype and E-Mail)	<ul style="list-style-type: none"> • Platform integration preparation • Application integration • Problem analysis and solution (consulting) • Bugfixing 	Please contact your local Bosch Rexroth distributor or country unit! *
Integration on-site support (including travel expenses)	<ul style="list-style-type: none"> • Code integration • Testing & Debugging • Acceptance testing 	Please contact your local Bosch Rexroth distributor or country unit! *
Alternatively DIEN-Engineering-Service R911297673	<ul style="list-style-type: none"> • Specific definition of tasks, efforts and conditions 	Flatrate (to be defined per project)

* Contact via Bosch Rexroth Web or send your inquiry to <mailto://Friedrich.Scheurer@boschrexroth.de> at DC-IA/SPC2

Friedrich Scheurer

Product Management Sercos Technology DC-IA/SPC2

Bosch Rexroth AG

The Drive & Control Company

Tel. +49(9352)18-5669

Mob. +49 172 37 24 635

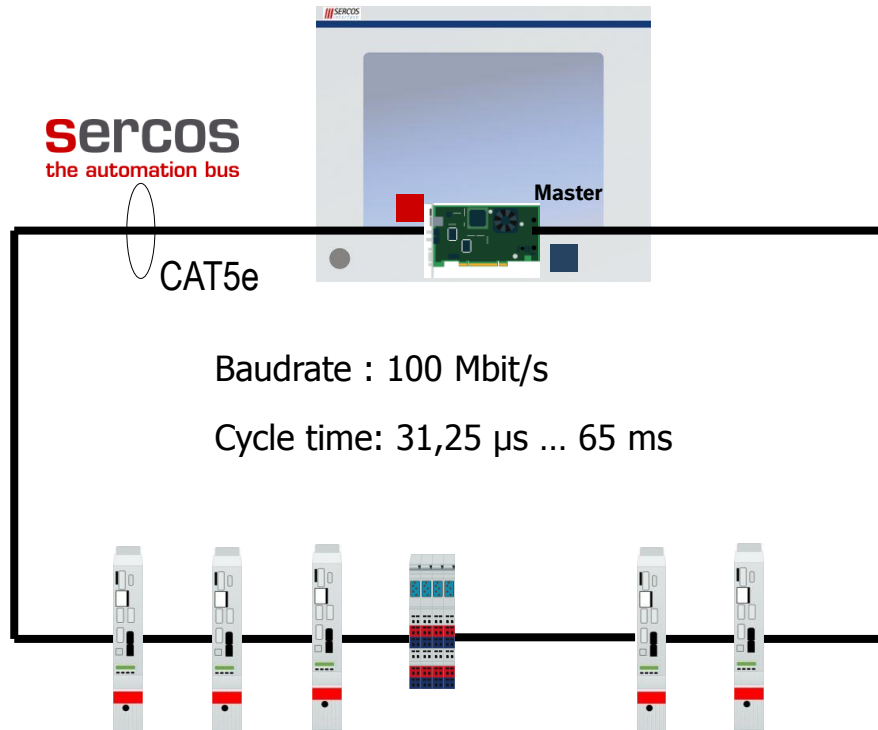
Mail: Friedrich.Scheurer@boschrexroth.de





Sercos Basics

Topology Overview



Fast Ethernet
(Full-Duplex)

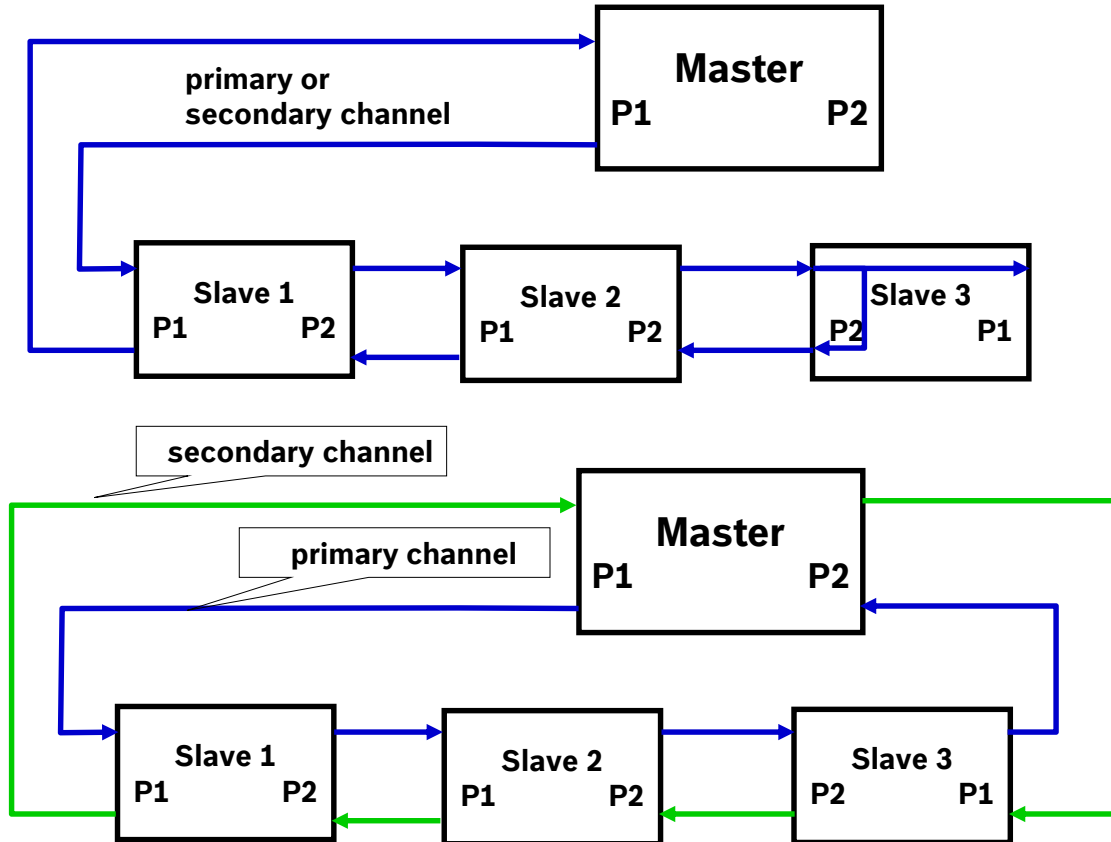
Standard
Ethernet
Frames

Line Topology

Ring Topology

Cyclic real-time
communication

Topology - Line and Ring



Line

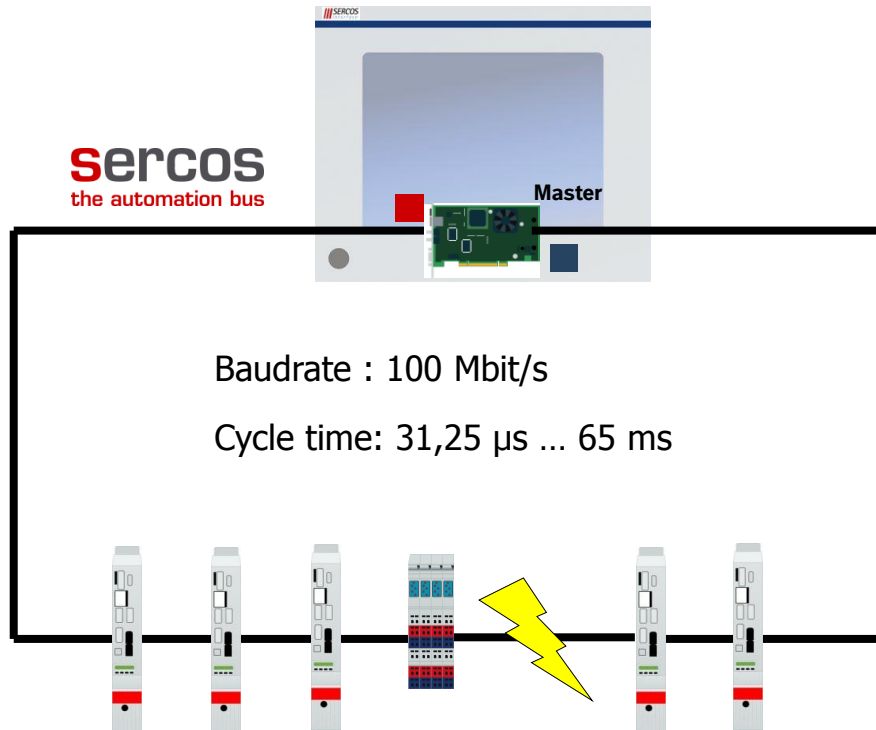
The master may process the data from the slaves on port P1 or P2

Ring

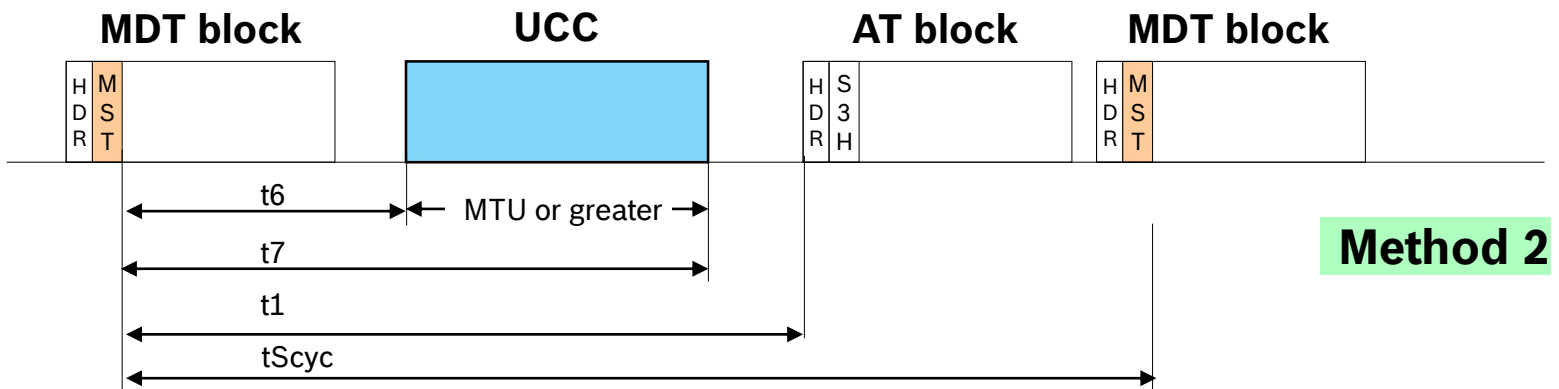
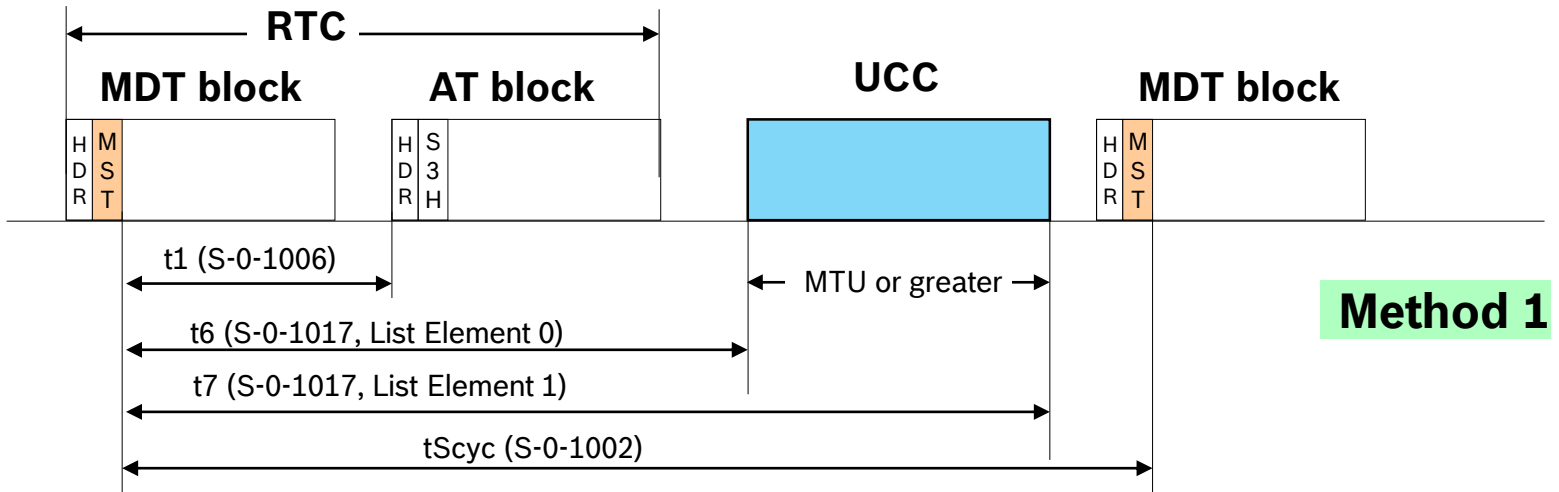
Cyclic redundant transmission of real-time data in primary and secondary channel

Redundancy Overview

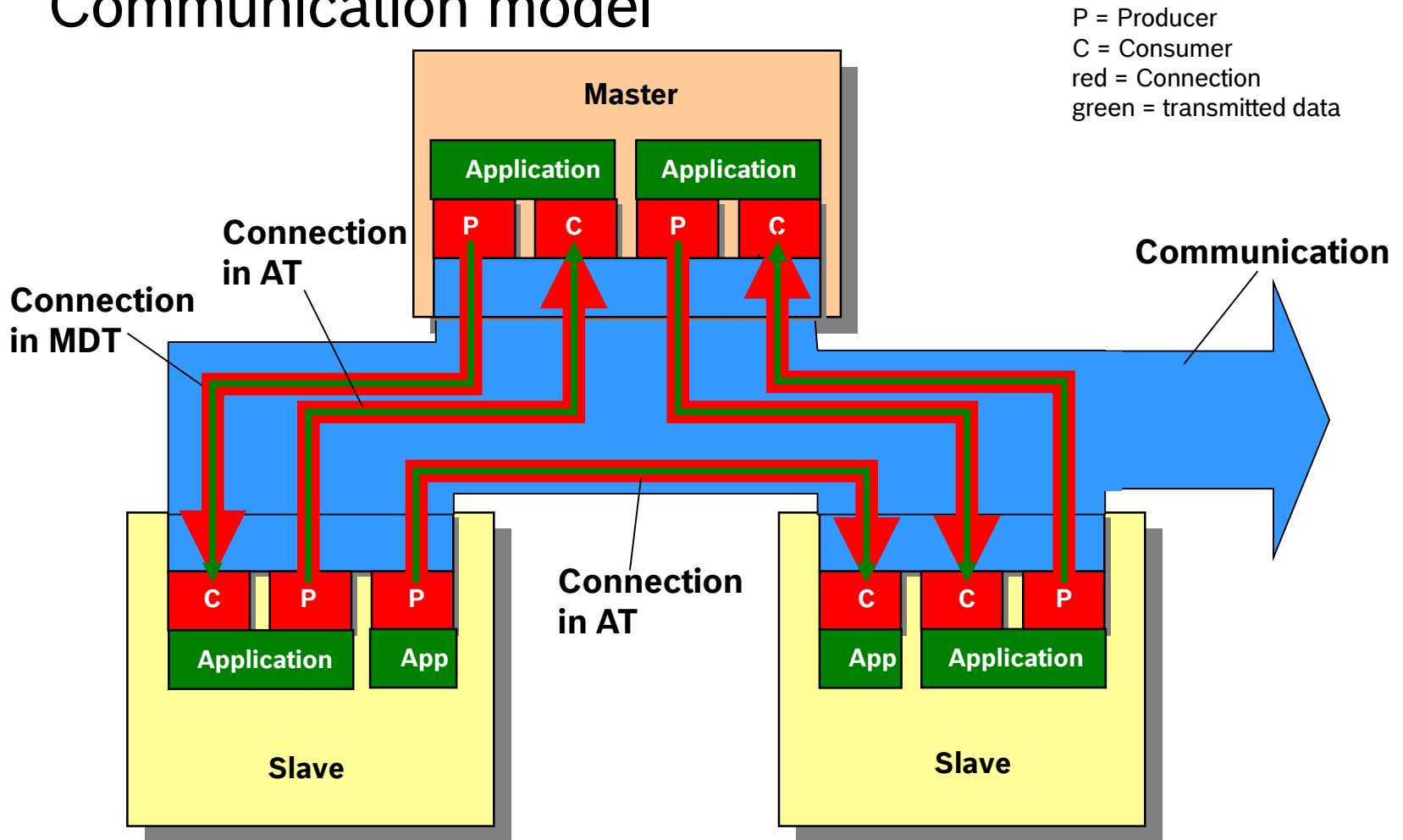
Redundancy,
e.g. Cable break



Communication Timing in CP3 and CP4

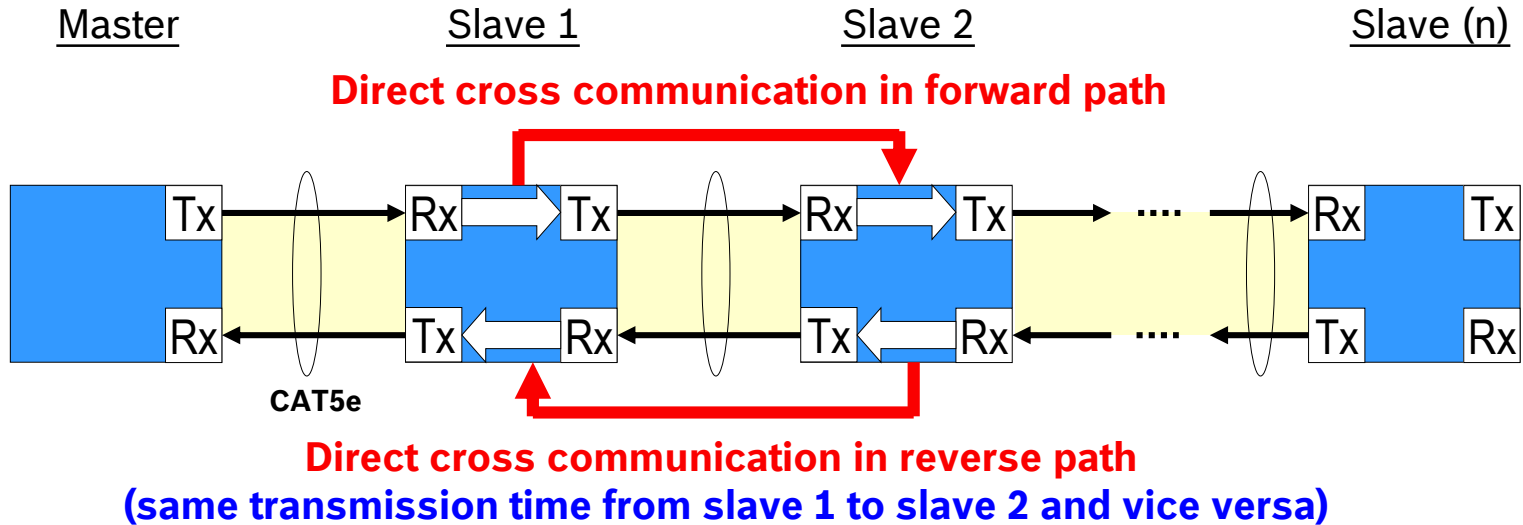




Communication model



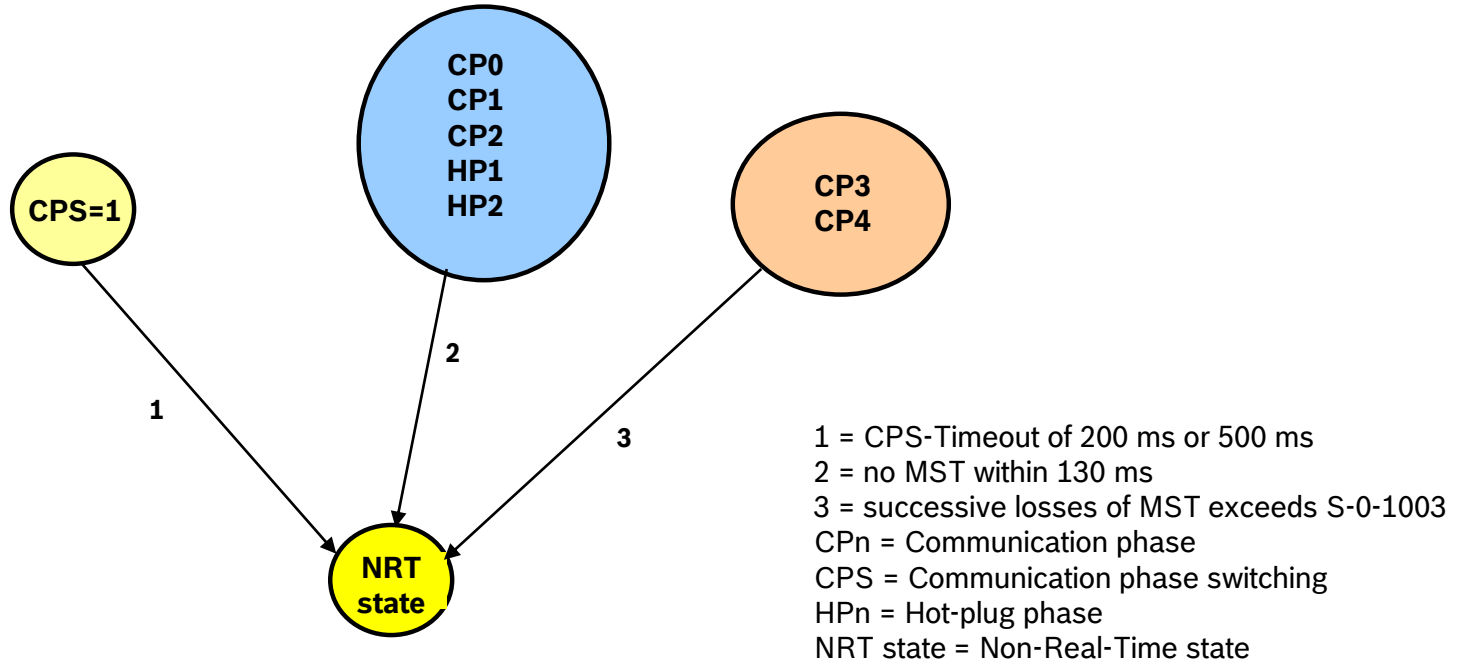
Sercos Cross Communication Concept

Real-time data exchanged in both directions within the same communication cycle



-  Real-time processing during passing through a node
-  Signal transmission without interpretation
- Tx: Ethernet transmitter, Physical Interface
- Rx: Ethernet receiver, Physical Interface

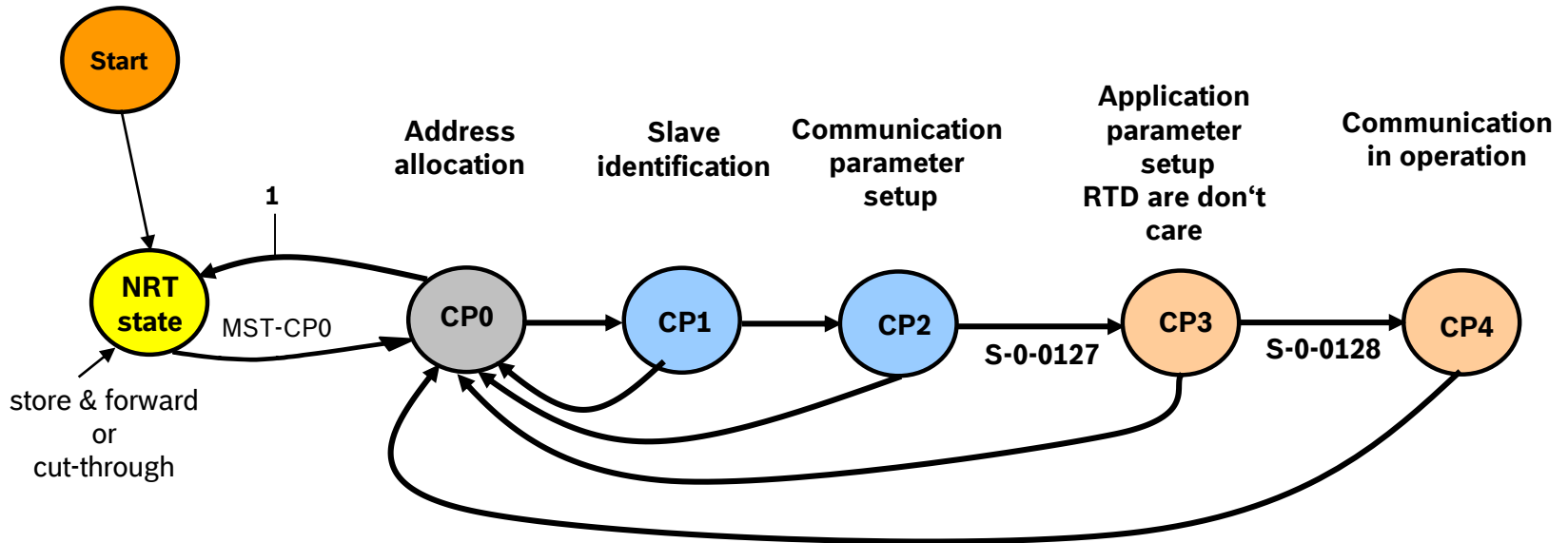
Communication error handling in CP0 to CP4, CPS, HP0 to HP2



Sercos III telegram loss (CP3/4) = if no valid telegram are received on P1 and P2 within one communication cycle

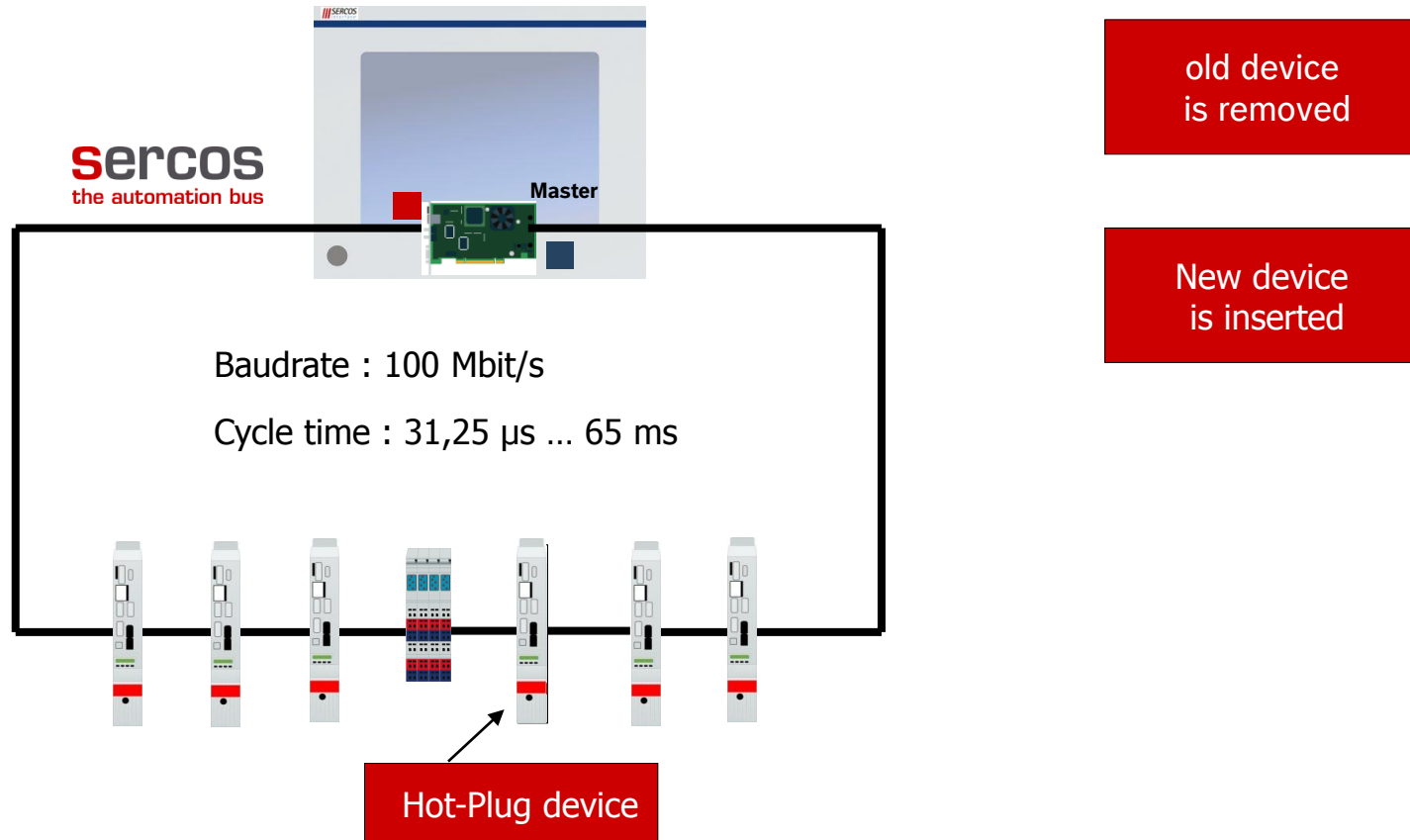
Initialization

Communication phases (CP0 to CP4)



1 = no MST-CP0 within 130 ms
CP = Communication phase
NRT state = Non-Real-Time state

Hot-plugging Overview



Hot-plugging Hot-plug phases (HP0 to HP2)

- Hot-plugging is possible with line only
- with ring, a ring break has to be initiated first
- Hot-plug function active on one channel only (P or S)
- Master shall be prepared for the HP slave
- Hot-plugging consists of 3 phases (HP0 to HP2)

1 = no MST-CP4 within 130 ms or HP not supported

2 = HP Slave activates Loopback

Master commands FF to last slave

3 = Master switches from HP field to SVC

4= Master activates timing of CP3/4 (S-0-0127)

5 = Master activates OL (S-0-0128)

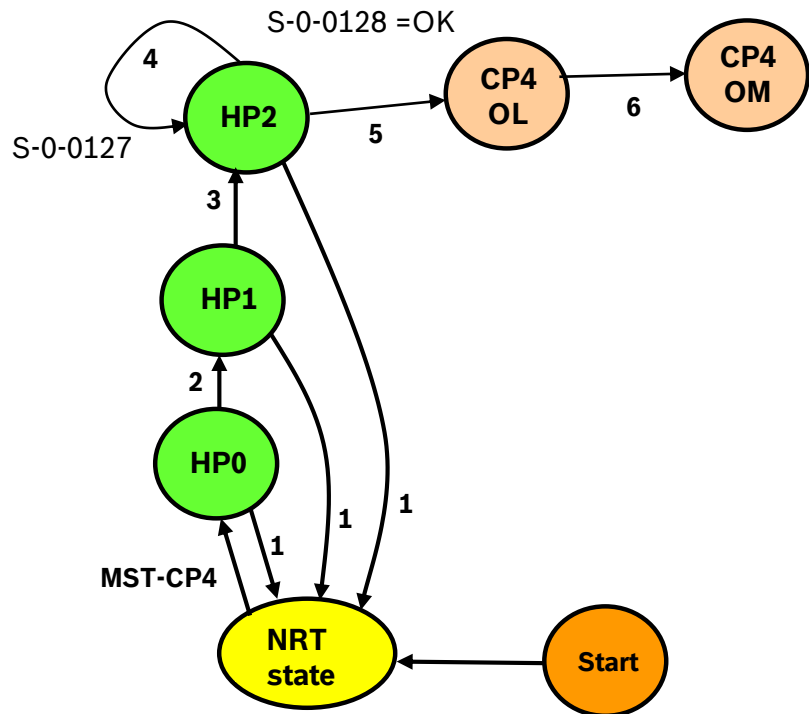
6 = Master activates operation mode

NRT state = Non-Real-Time state

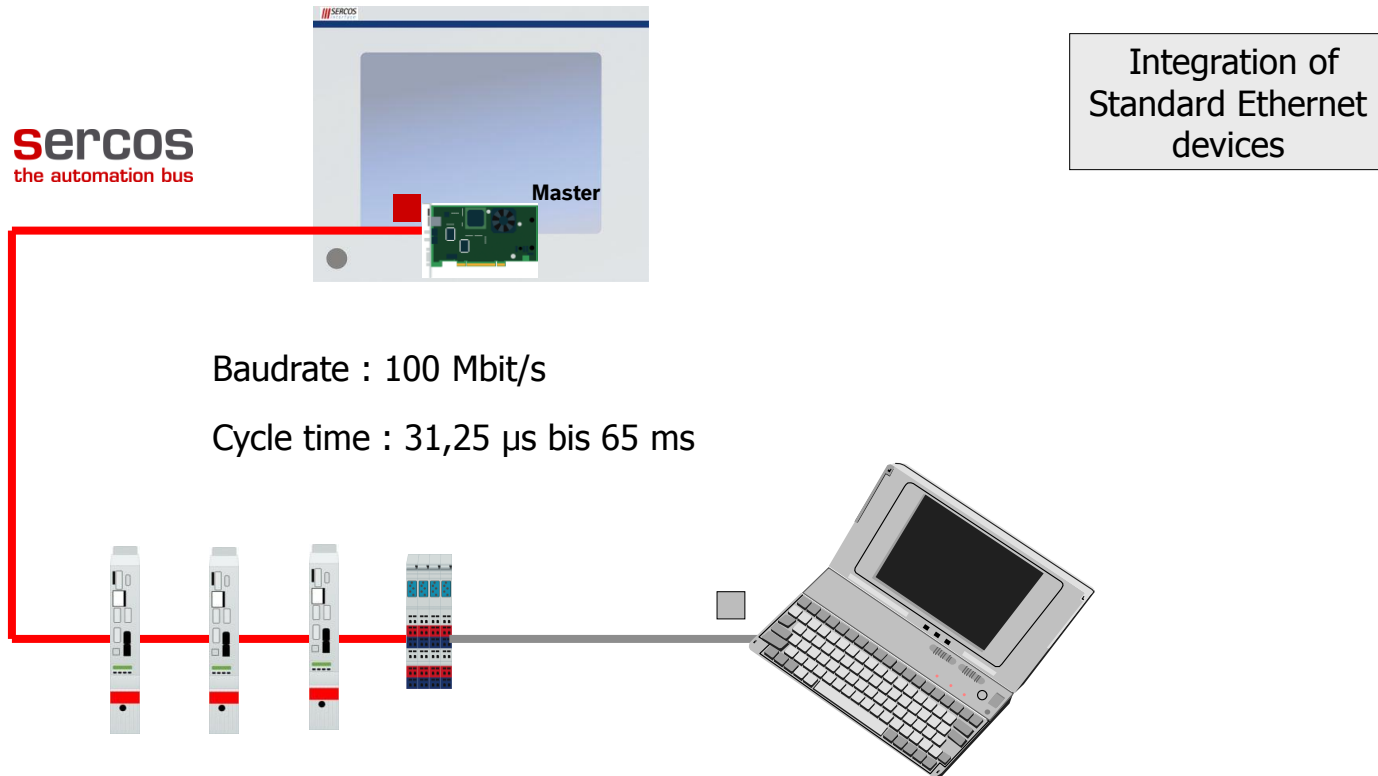
HP = Hot-plugging

OL = Operating level

OM = Operation mode



IP Communication - Overview



IP Communication

Service defines exchange of Sercos III data

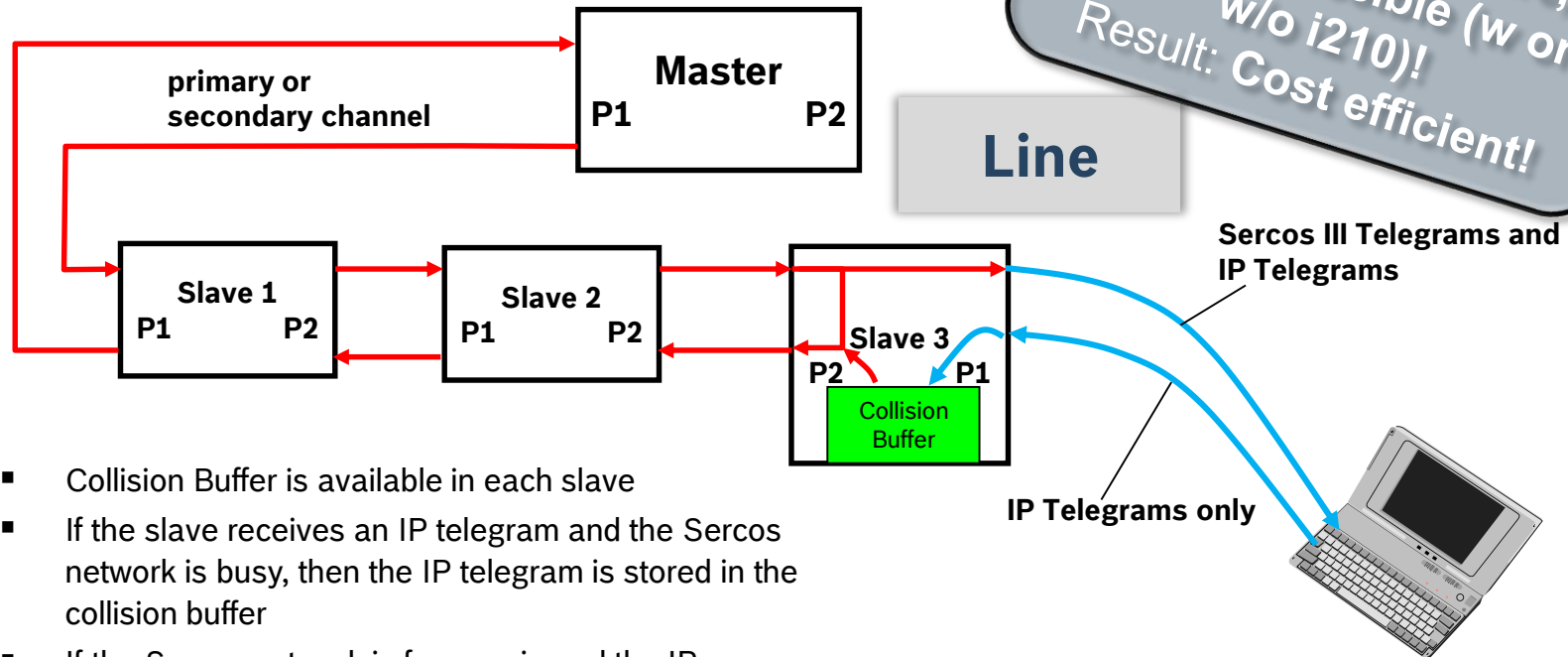
- TFTP (trivial file transfer protocol)
 - Standard protocol based on UDP
 - TCP / IP Stack isn't necessary
 - used for firmware download

- Sercos Internet Protocol S/IP
 - Standard protocol between nodes
 - Defined on TCP/IP and UTP/IP
 - This protocol leads on mechanism of OPC-UA.
 - Protocol is under verification
 - used to modify and setup parameters in the slave

TFTP (Trivial File Transfer Protocol)	
Familie:	TCP/IP
Einsatzgebiet:	Laden von Betriebssystemen und anderen Daten über das Netzwerk
Ports:	69/UDP
TFTP im TCP/IP-Protokollstapel:	
Anwendung	TFTP
<i>Transport</i>	UDP
<i>Internet</i>	IP (IPv4, IPv6)
<i>Netzwerk</i>	Ethernet Token Bus Token Ring FDDI ...
Standards:	RFC 1350 🔗 (1992)

S/IP	
Familie:	Internetprotokollfamilie
Einsatzgebiet:	Datenübertragung, Dateiverwaltung
Port:	20/TCP DATA Port, 21/TCP Control Port
FTP im TCP/IP-Protokollstapel:	
Anwendung	SIP
<i>Transport</i>	TCP
<i>Internet</i>	IP (IPv4, IPv6)
<i>Netz-Zugang</i>	Ethernet Token Bus Token Ring FDDI ...
Standards:	RFC 959 🔗 (1985)

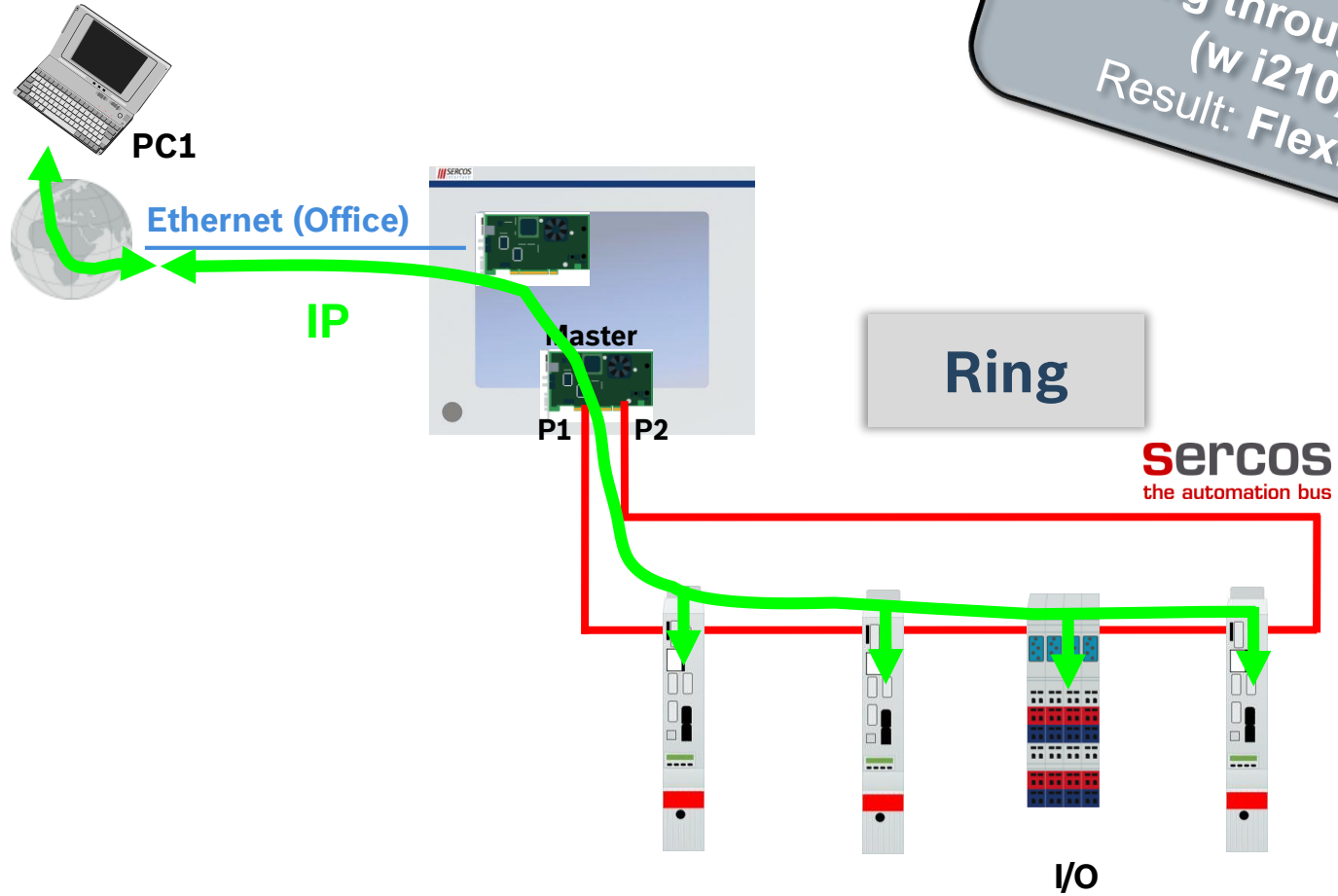
IP Communication - General



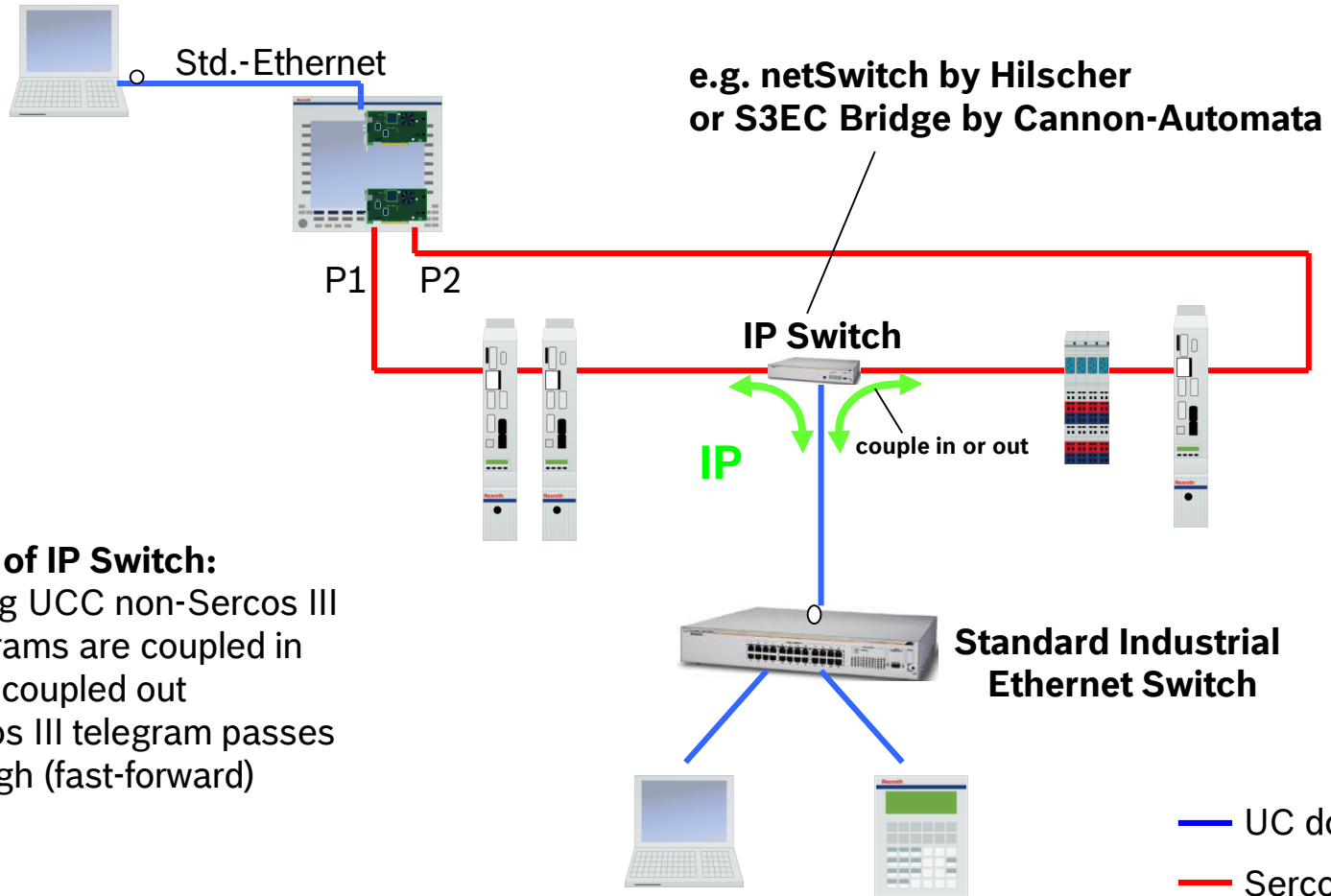
- Collision Buffer is available in each slave
- If the slave receives an IP telegram and the Sercos network is busy, then the IP telegram is stored in the collision buffer
- If the Sercos network is free again and the IP telegram is not consumed by the slave itself, then the IP telegram is automatically forwarded.
- PC receives Sercos III telegrams and IP telegrams
- PC transmits IP telegrams only

IP Communication - Ring topology

Advantage:
Redundancy, UCC
Routing through Master
(w i210)!
Result: Flexible!



IP Communication – IP switch with ring topology

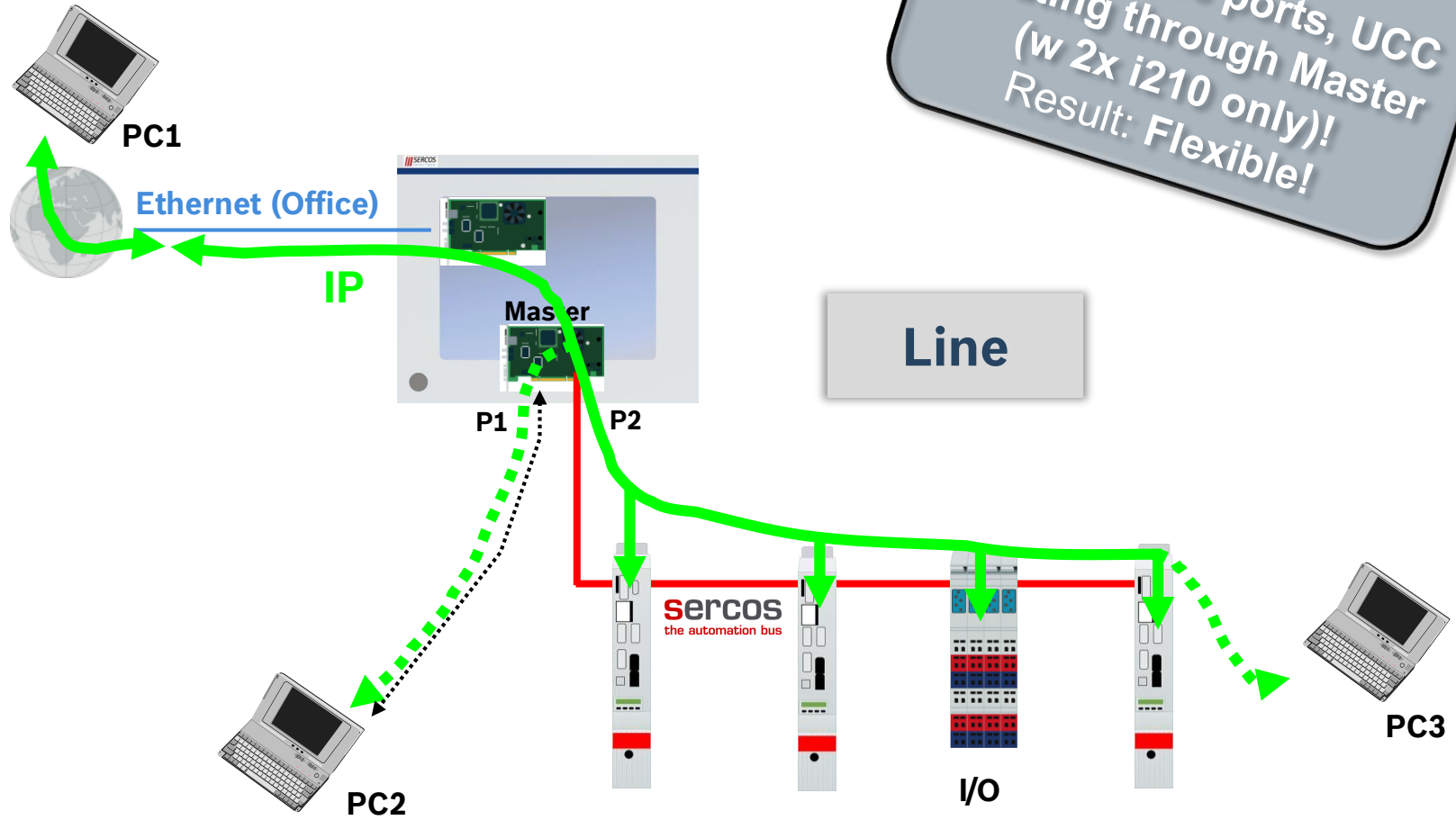


Function of IP Switch:

- During UCC non-Sercos III telegrams are coupled in resp. coupled out
- Sercos III telegram passes through (fast-forward)

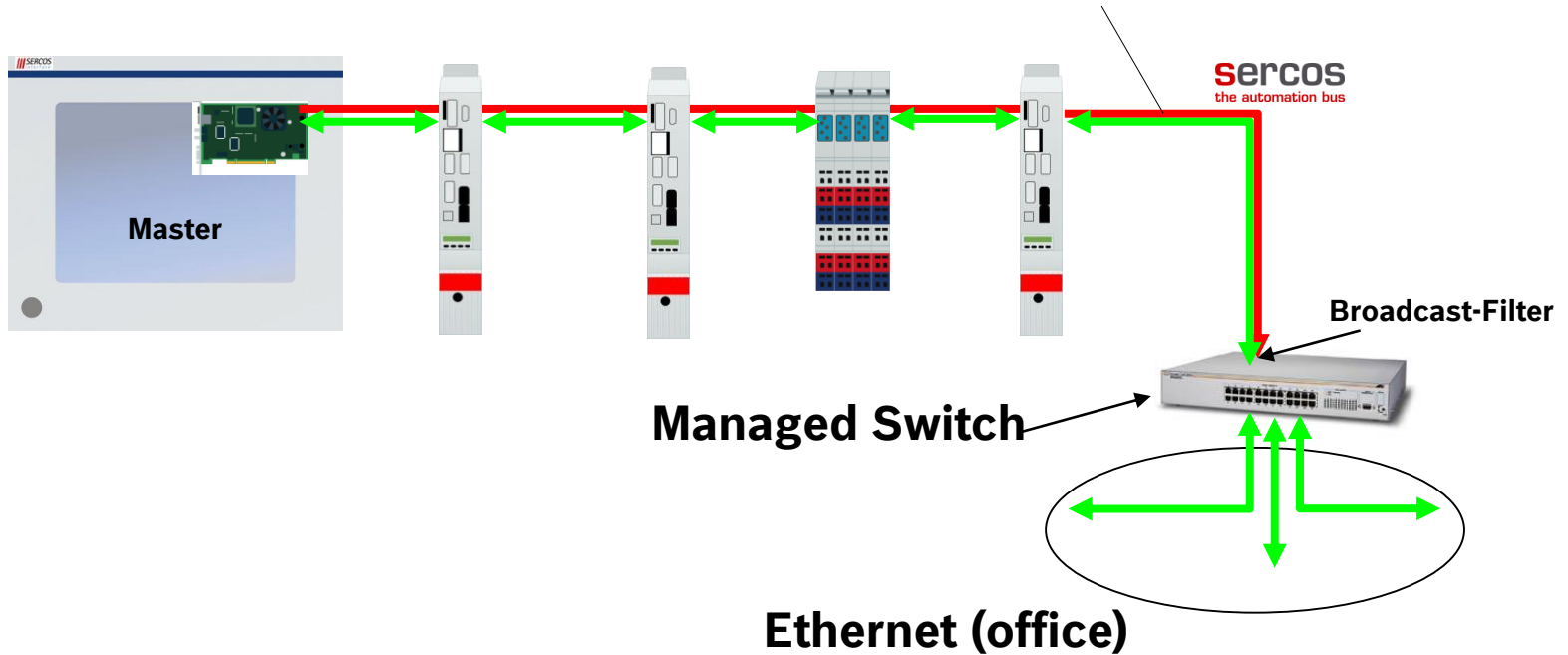
IP Communication - Line topology

Advantage:
Multiple free ports, UCC
Routing through Master
(w 2x i210 only!)
Result: Flexible!



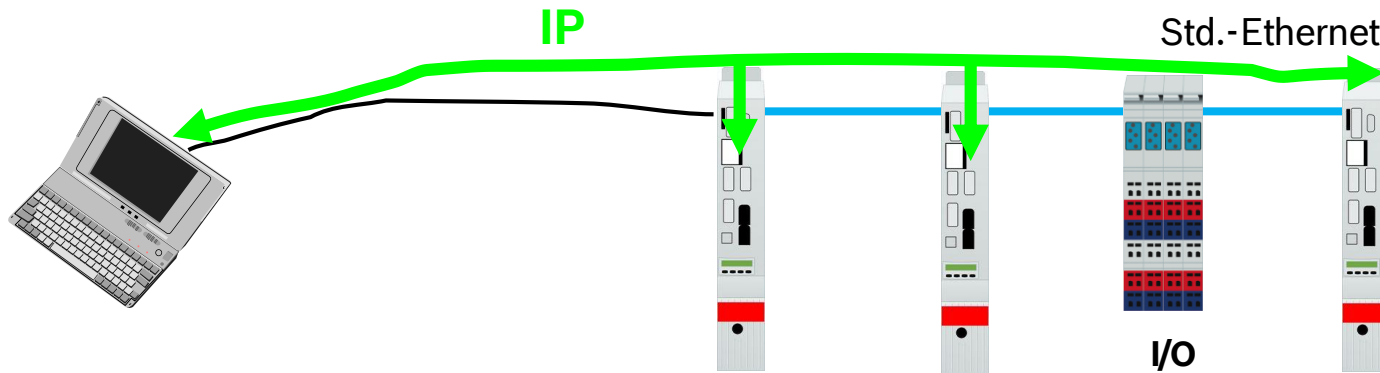
IP Communication - Line with Managed Switch

S III telegrams using Broadcast address



IP Communication

Commissioning without Sercos III communication



- Download/Upload of parameters via S/IP possible
- Firmware download via TFTP possible
- PC is connected to a unused Sercos III port
- Address assignment with „zero-conf-service“ (part of DHCP)
- IP addresses are listed in the PC
- LED flashes, if slave is addressed

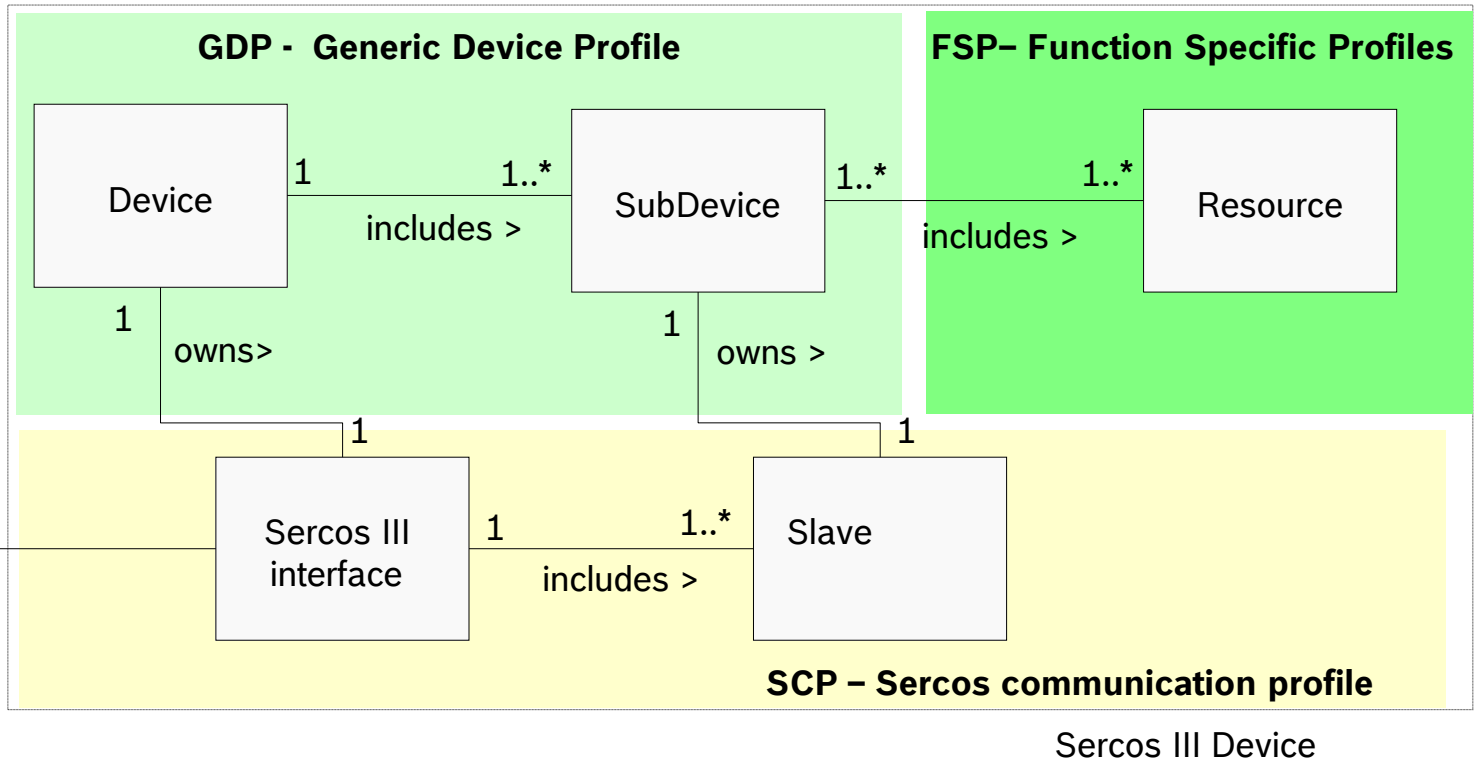
S/IP = defined services, e.g. how to read and write parameters

Communication performance

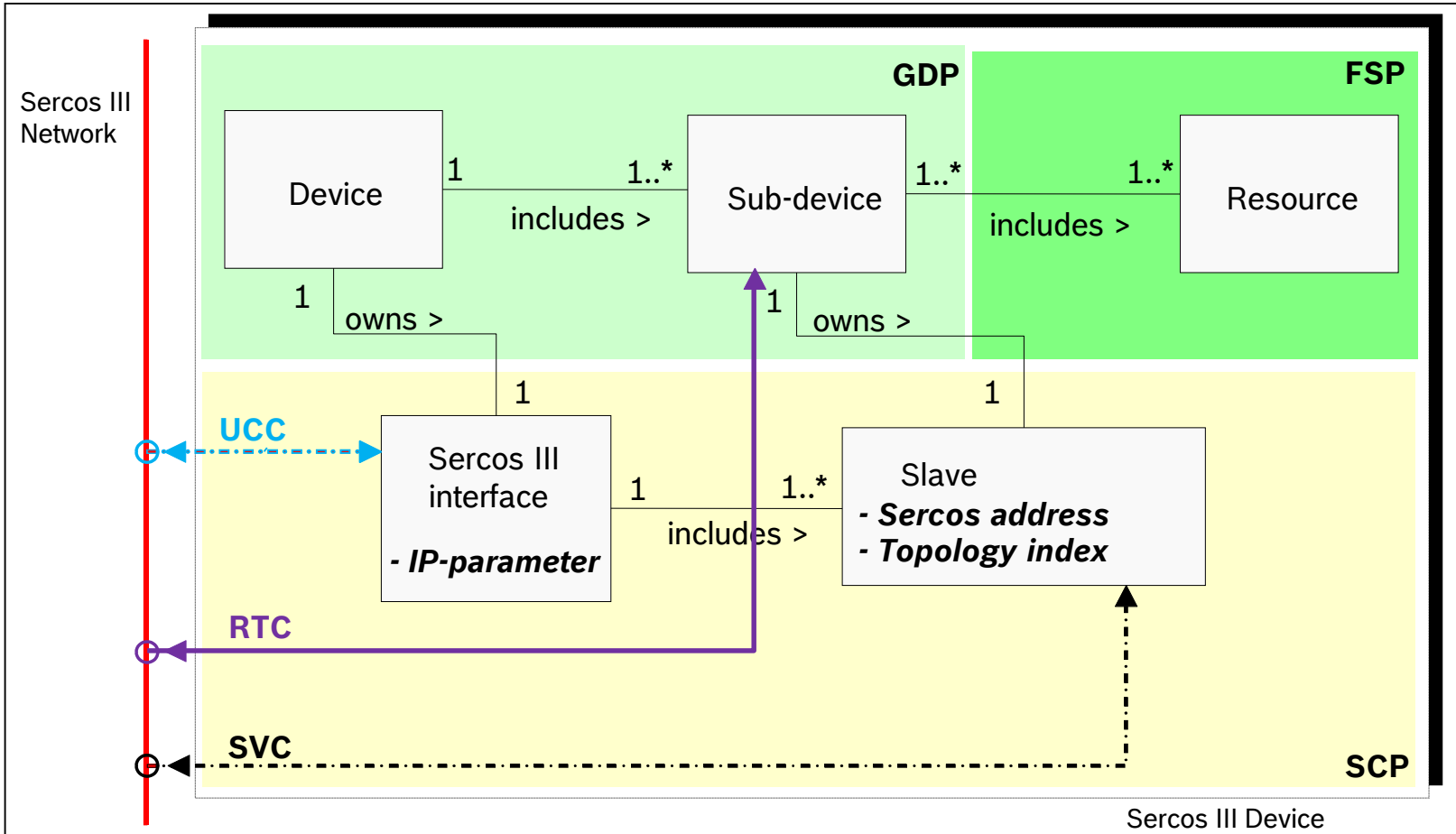
Cycle time [μs]	Cyclic data [Byte]	Number of Slaves			Number of MDT / AT
		UCC not used	UCC 1500 Byte 125 μs	UCC 250 Byte 20 μs	
31,25	8	7	--	2	1/1
62,5	12	14	--	8	1/1
125	16	26	--	21	1/1
250	12	61 (10)	30	57	1/1
250	32	33	17	31	1/1
500	12	122 (20)	94	120	2/2
1000	12	251	220	245	4/4
1000	32	137	120	134	4/4
1000	50	97	85	95	4/4

Communication and device profiles

Sercos III Network



Addressing scheme / logical comm. channels

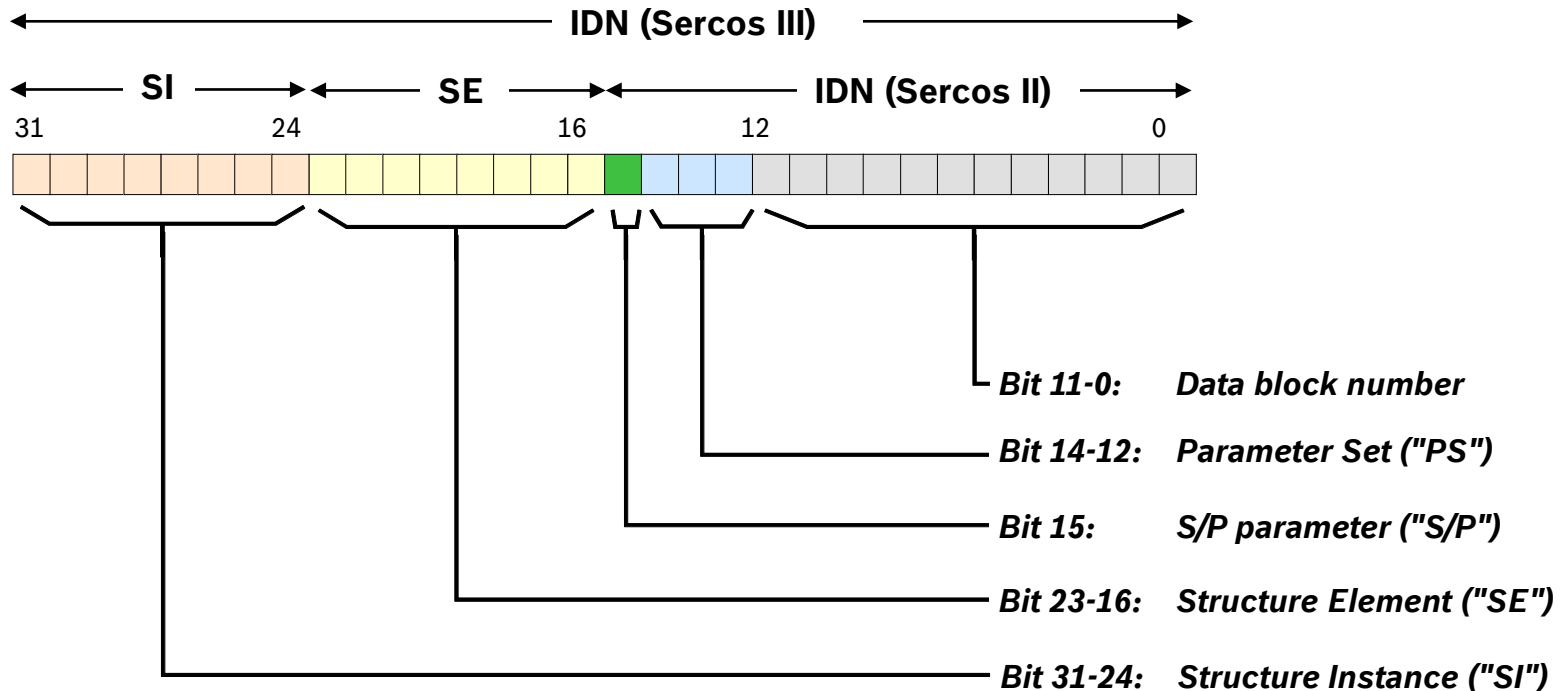


Parameter Structure (Identification Numbers IDN)

- Every parameter can consist of up to 7 elements.
- 3 elements are mandatory

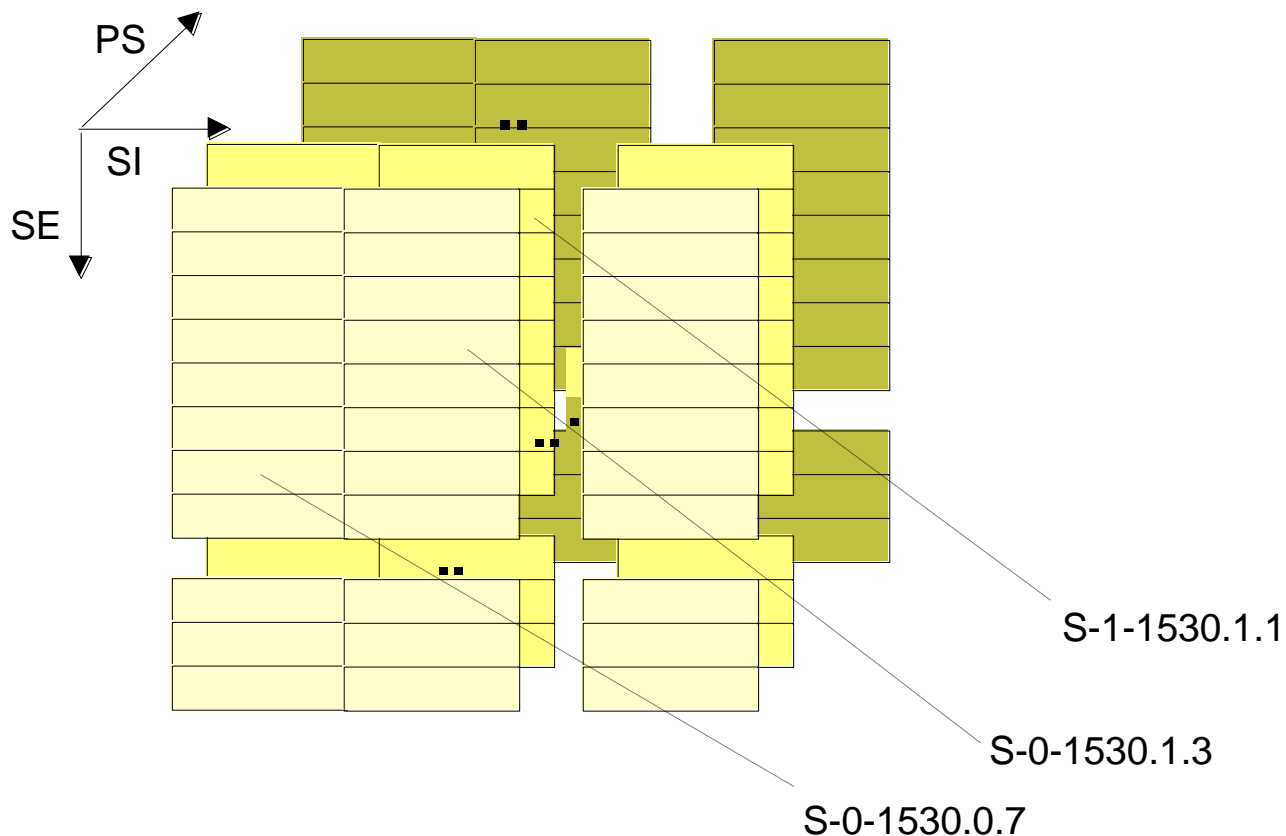
Element No.	Description	Requirement
1	IDN	mandatory
2	Name	optional
3	Attribute	mandatory
4	Unit	optional
5	Min. Value	optional
6	Max. Value	optional
7	Data	mandatory

Identification Number – IDN (Element 1: write access)



- **< IDN>.<SI>.<SE>** e.g. S-0-1530.2.5
- **SE: 0-127, are used for Standard parameters**
- **SE: 128-255, are usable for product specific parameters**
- **SI: 0-255, addressing the structure of the same type (instance)**

Addressing area with SI, SE and Parameter set


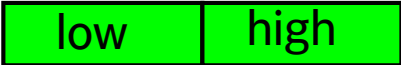
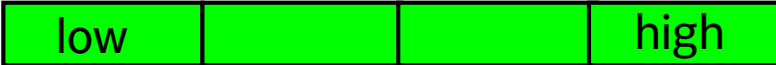


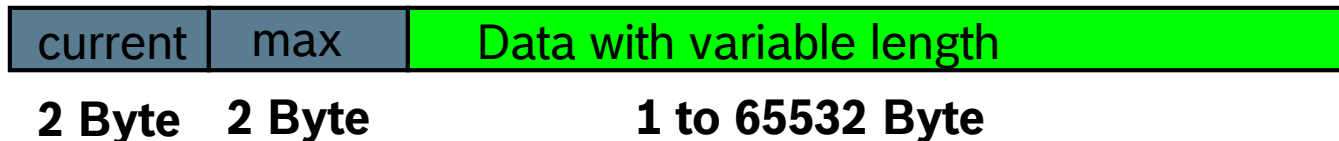
Attribute (Element 3)

- **Display format**
 - binary
 - unsigned decimal
 - signed decimal
 - hexadecimal
 - floating point
 - text (UTF8)
 - IDN
 - Time
- **Data length**
 - fixed with 2, 4, 8 byte
 - variable with list elements of 1, 2, 4, 8 byte
- **Write protection**
- **Conversion factor**
- **Decimal point**

Data length	Display format						
	binary	unsigned dec	signed dec	hex	text	IDN	float
2 byte	X	X	X	X			
4 byte	X	X	X	X		X	X
8 byte	X	X	X	X			X
1 byte list	X	X		X	X		
2 byte list	X	X	X	X			
4 byte list	X	X	X	X		X	X
8 byte list	X	X	X	X			X

Operation Data (Element 7)

- 2 Byte 
- 4 Byte 
- 8 Byte 
- variable Length



Data Scaling of Operation data

- Scaling for all Position data
- Scaling for all Velocity data
- Scaling for all Torque/Force data
- Scaling for all Acceleration/Jerk data

Data Scaling - Scaling Parameter

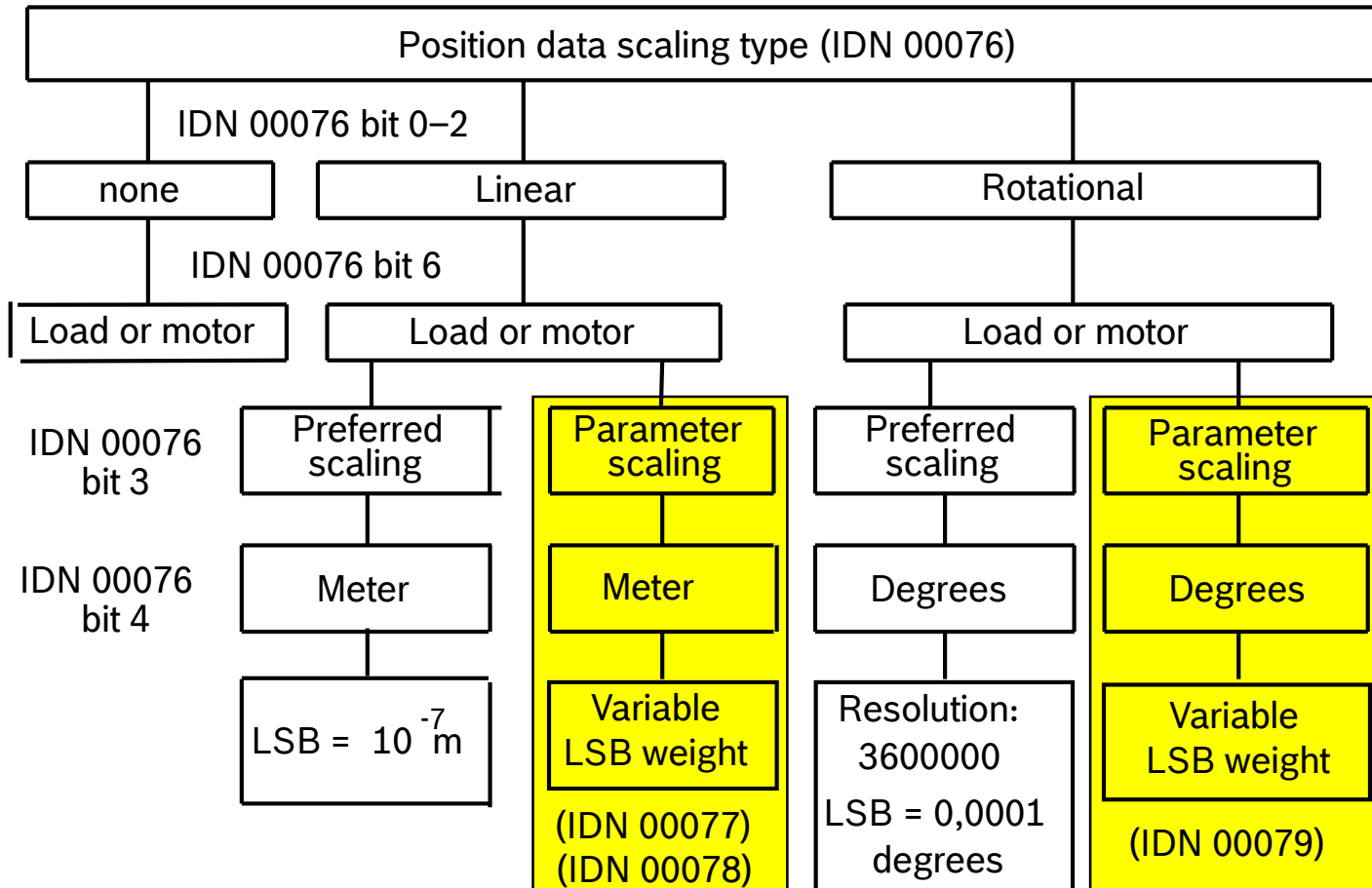
- Scaling Type
- Scaling Factor
- Scaling Exponent

- Rotational Position Resolution (for position data only)

Data Scaling - Scaling selections

- Scaling types
 - No scaling
 - Linear
 - Rotational
- Preferred Scaling
- Parameter Scaling
- Data reference
 - Motor shaft
 - Load
- Processing format
 - absolute
 - modulo

Position Scaling Diagram



More information

- Sercos [eLearning](#)
- Sercos technology [overview](#) web
- Sercos [brochures](#)
- Sercos [webinars](#)
- Sercos [specifications](#) in Wiki (Register [here!](#))

Rexroth

Thank you for
Considering

Sercans solutions!